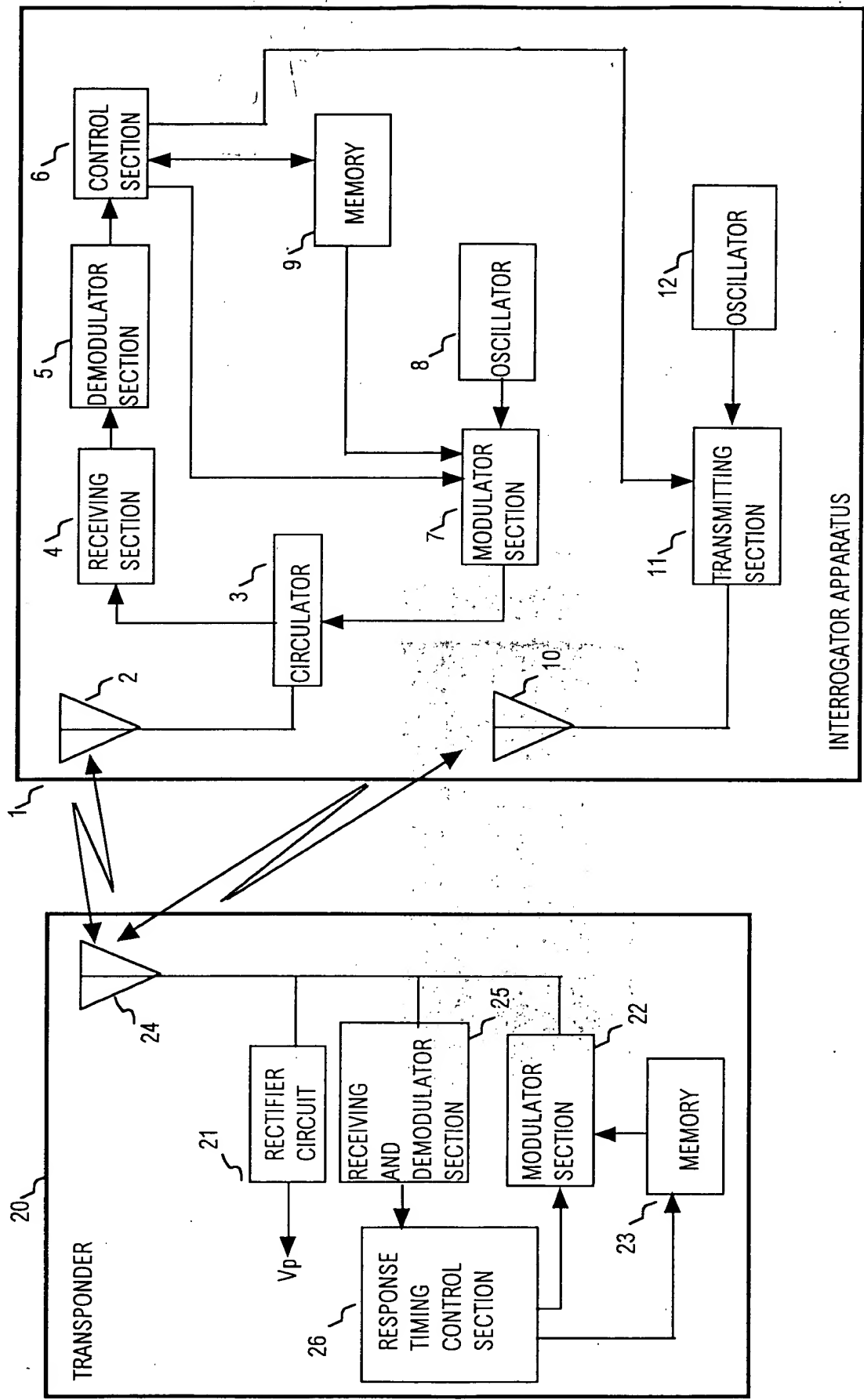
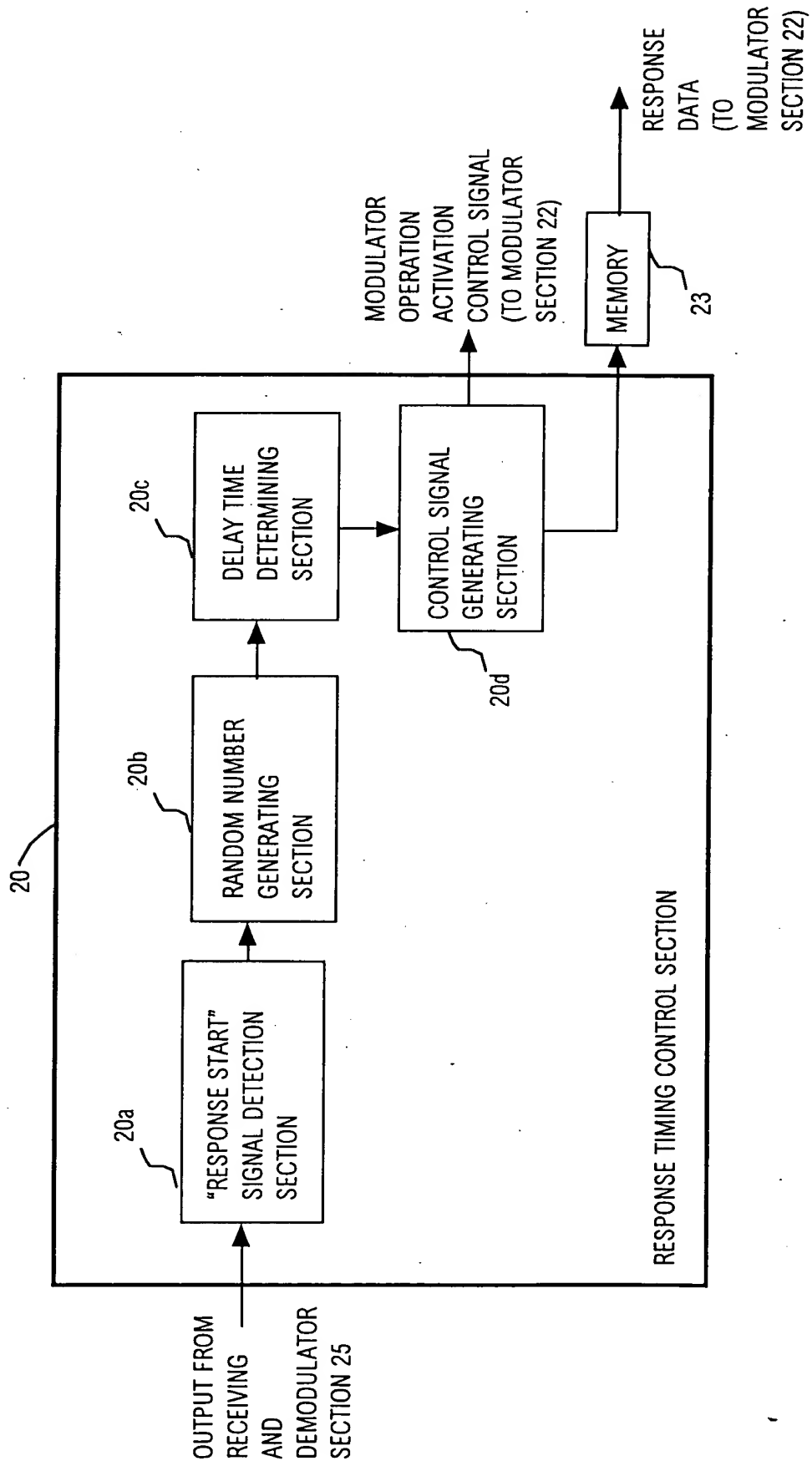


FIG. 1



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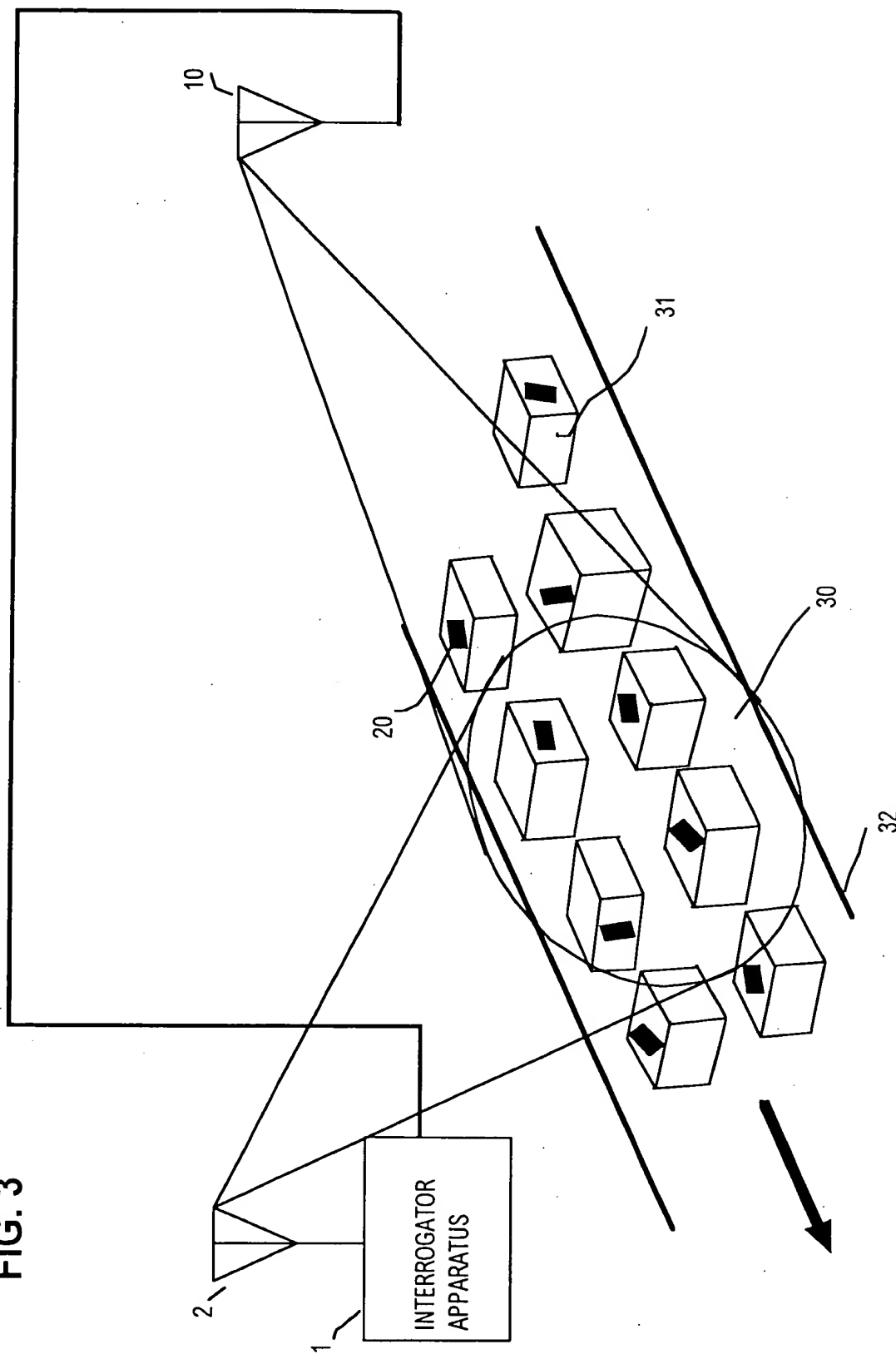
FIG. 2



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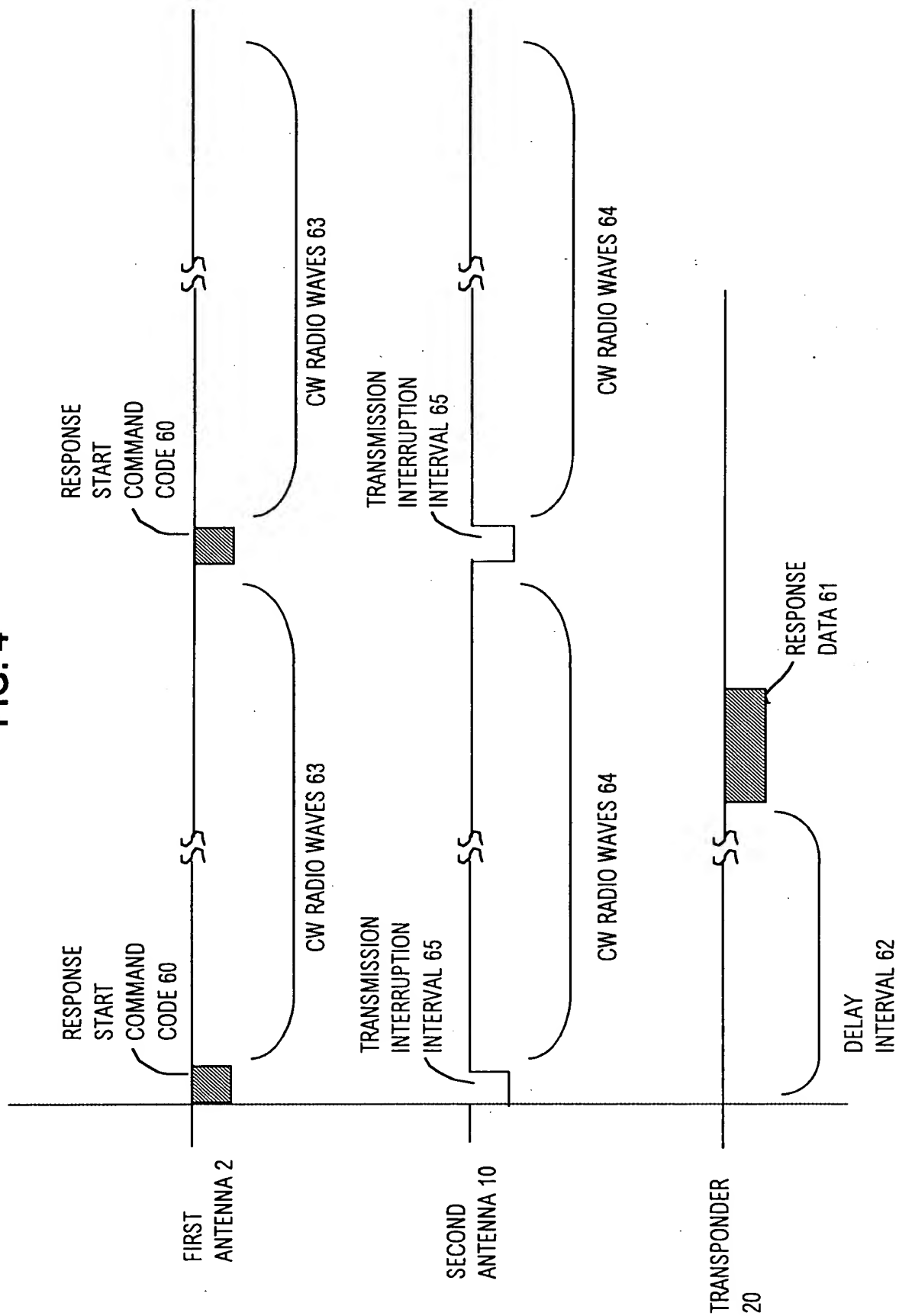
FIG. 3



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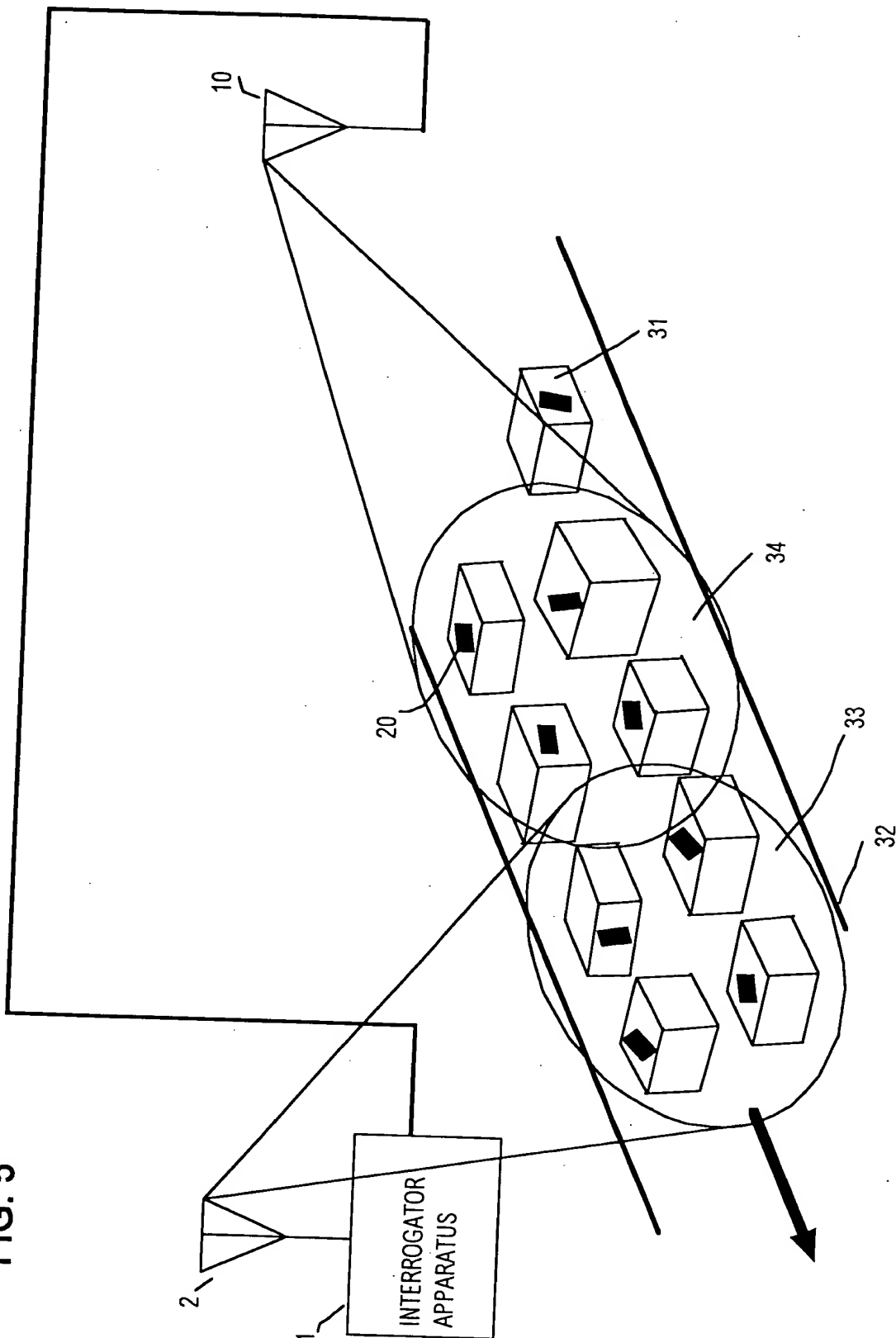
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FIG. 4



**Abstract**

**FIG. 5**



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FIG. 6

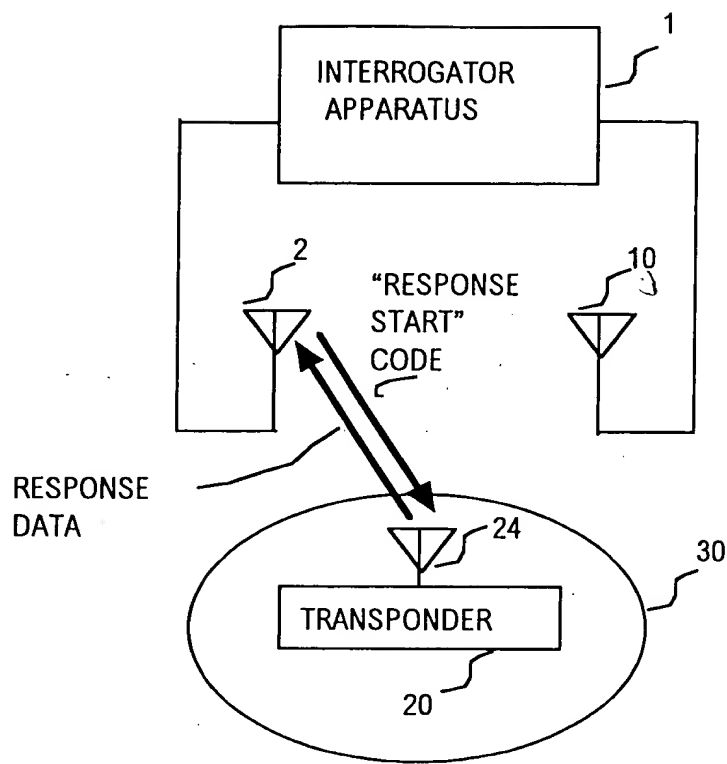
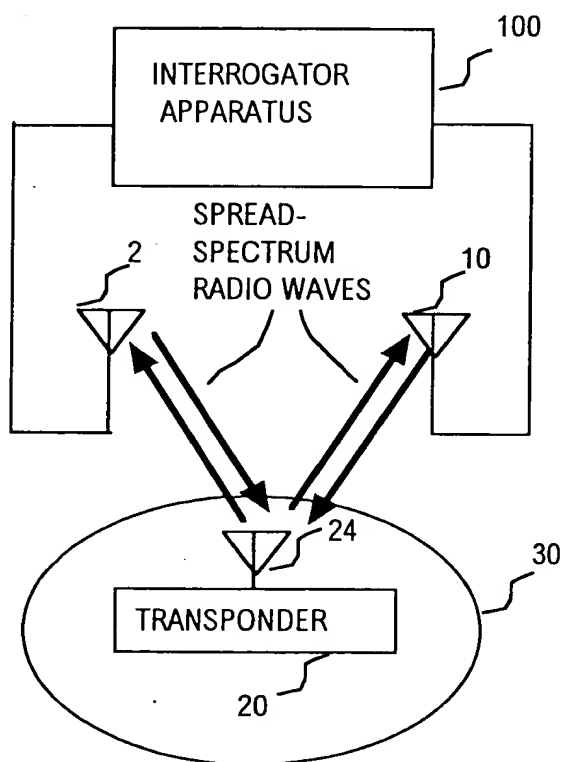
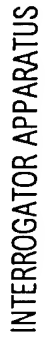


FIG. 7



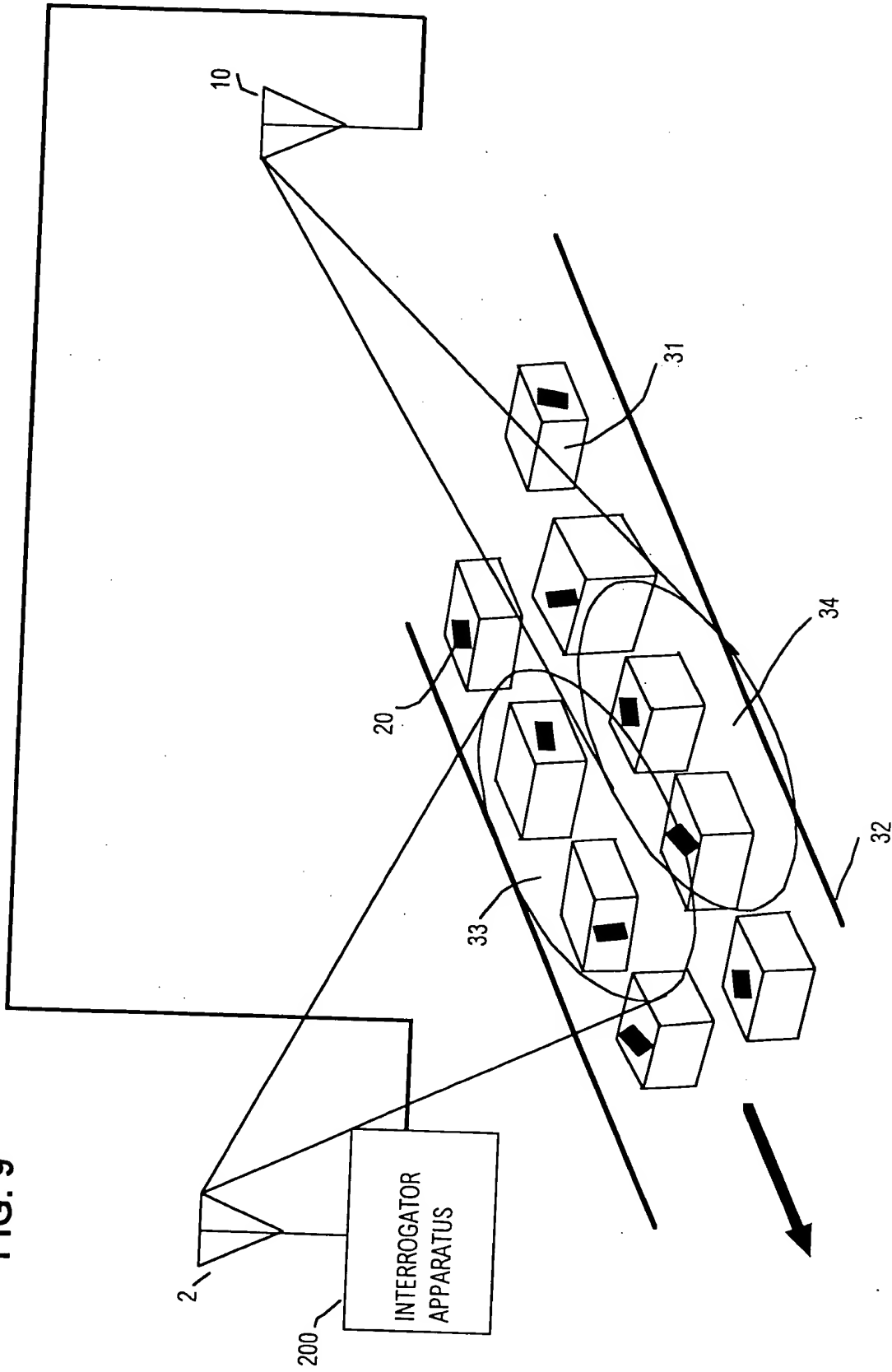
**FIG. 8**



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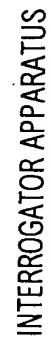
FIG. 9





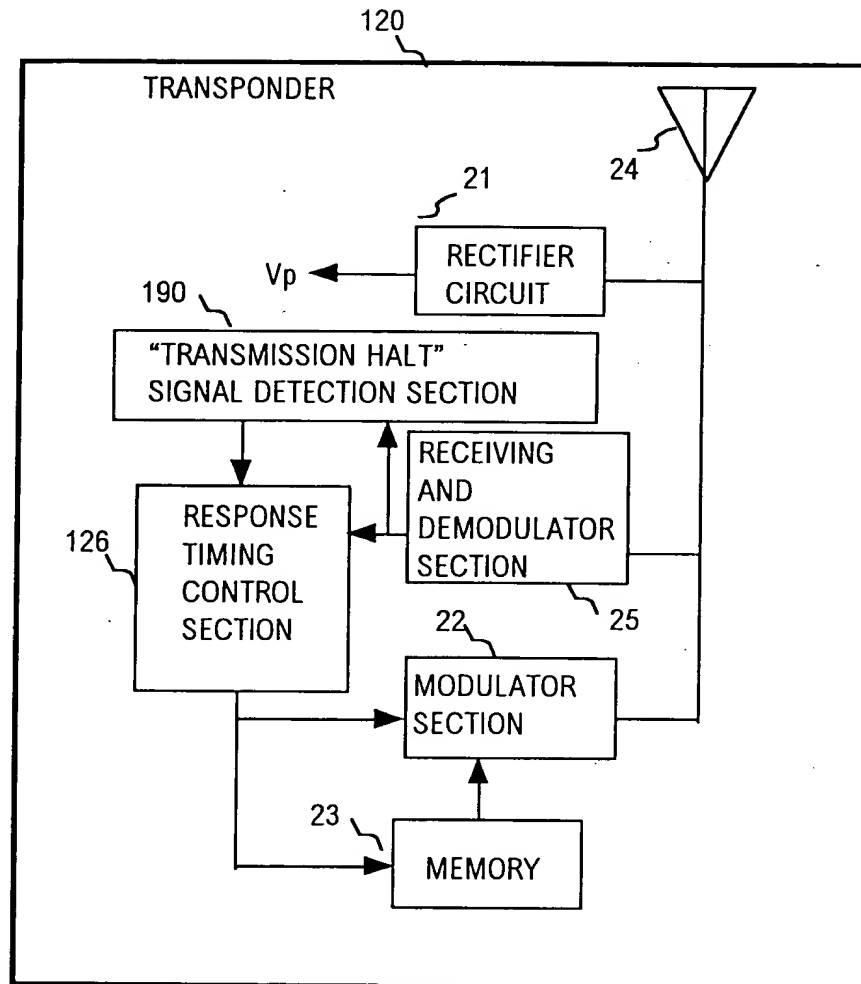
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**FIG. 10**



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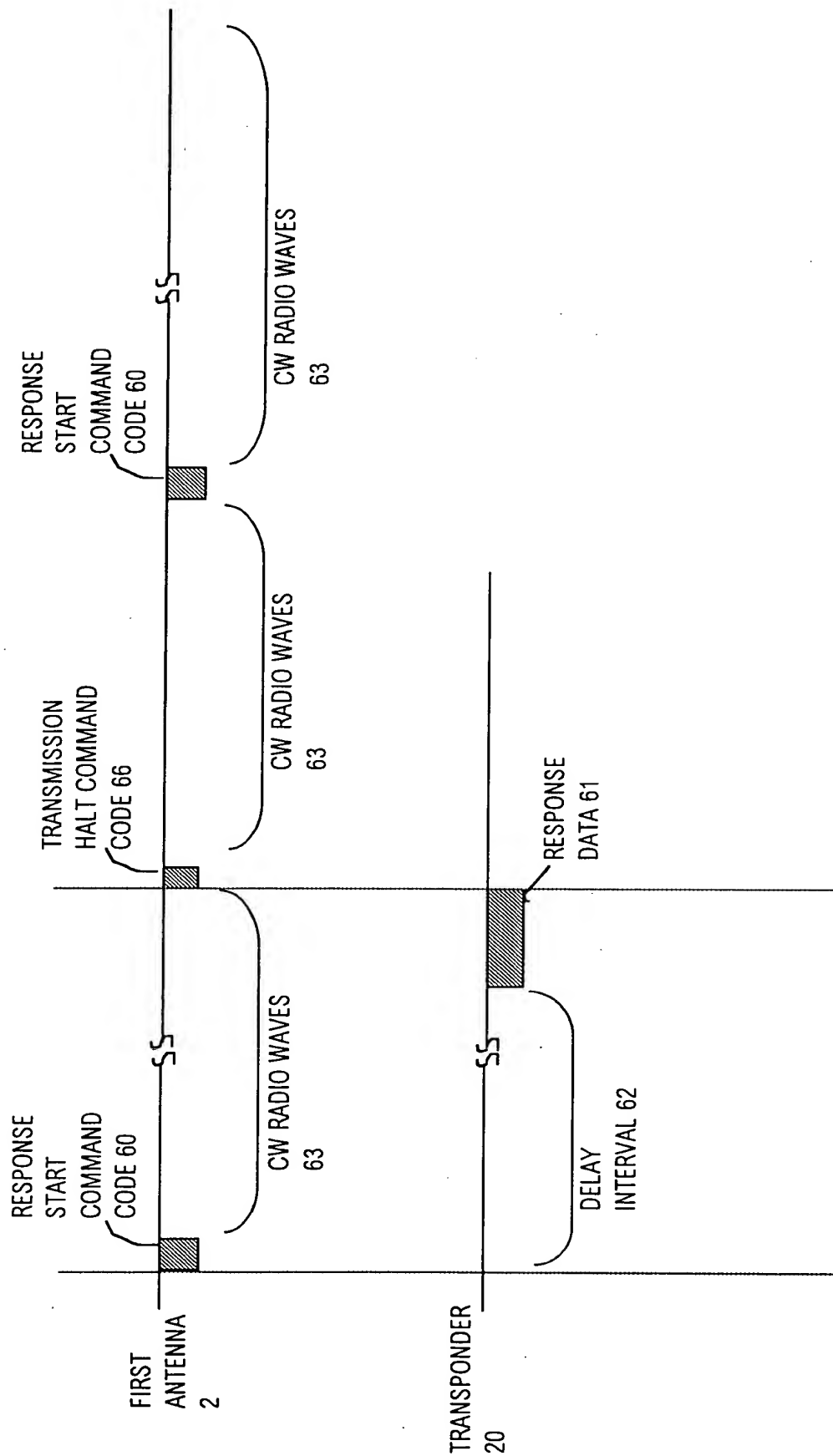
FIG. 11



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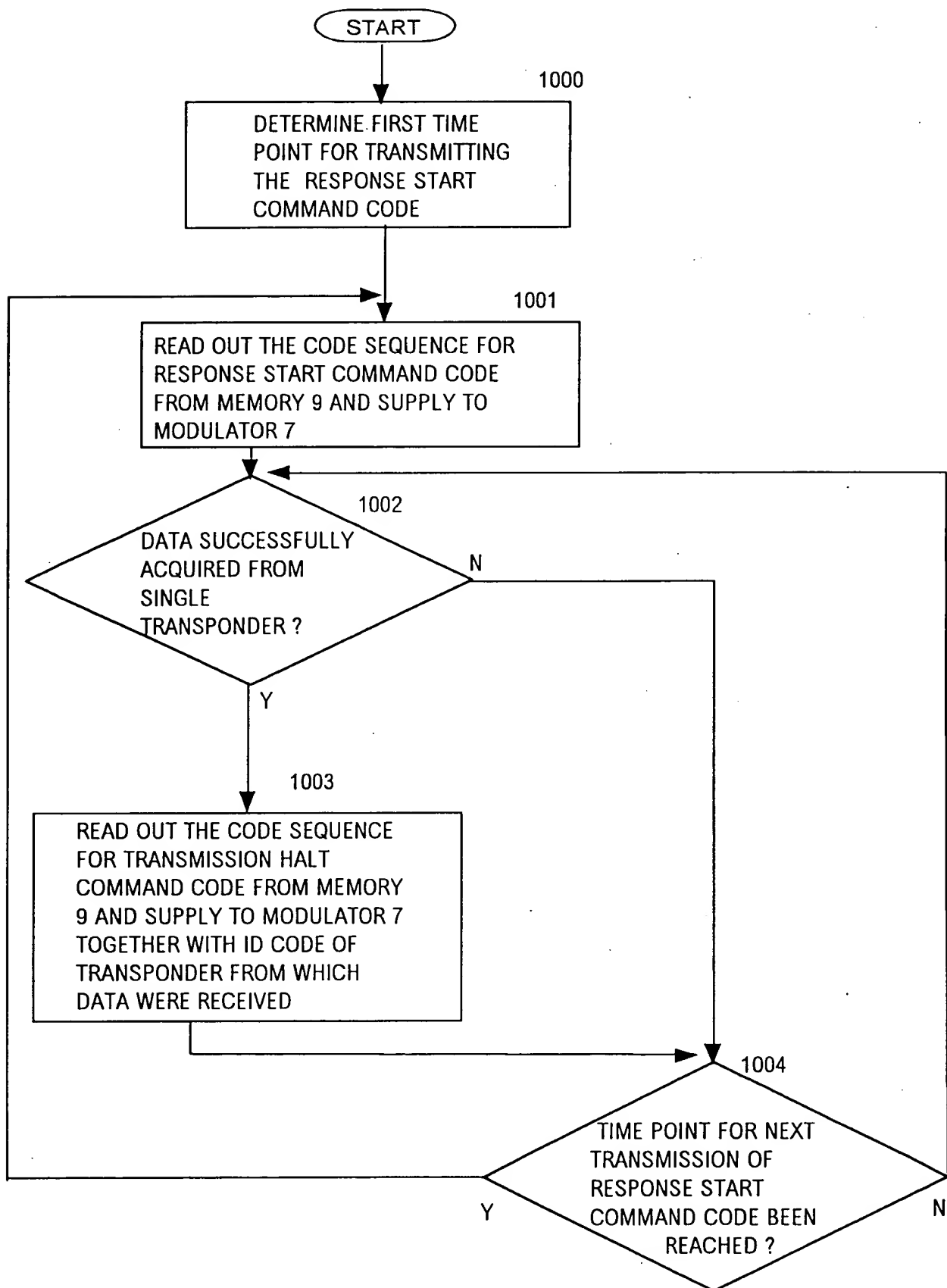
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FIG. 12



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FIG. 13



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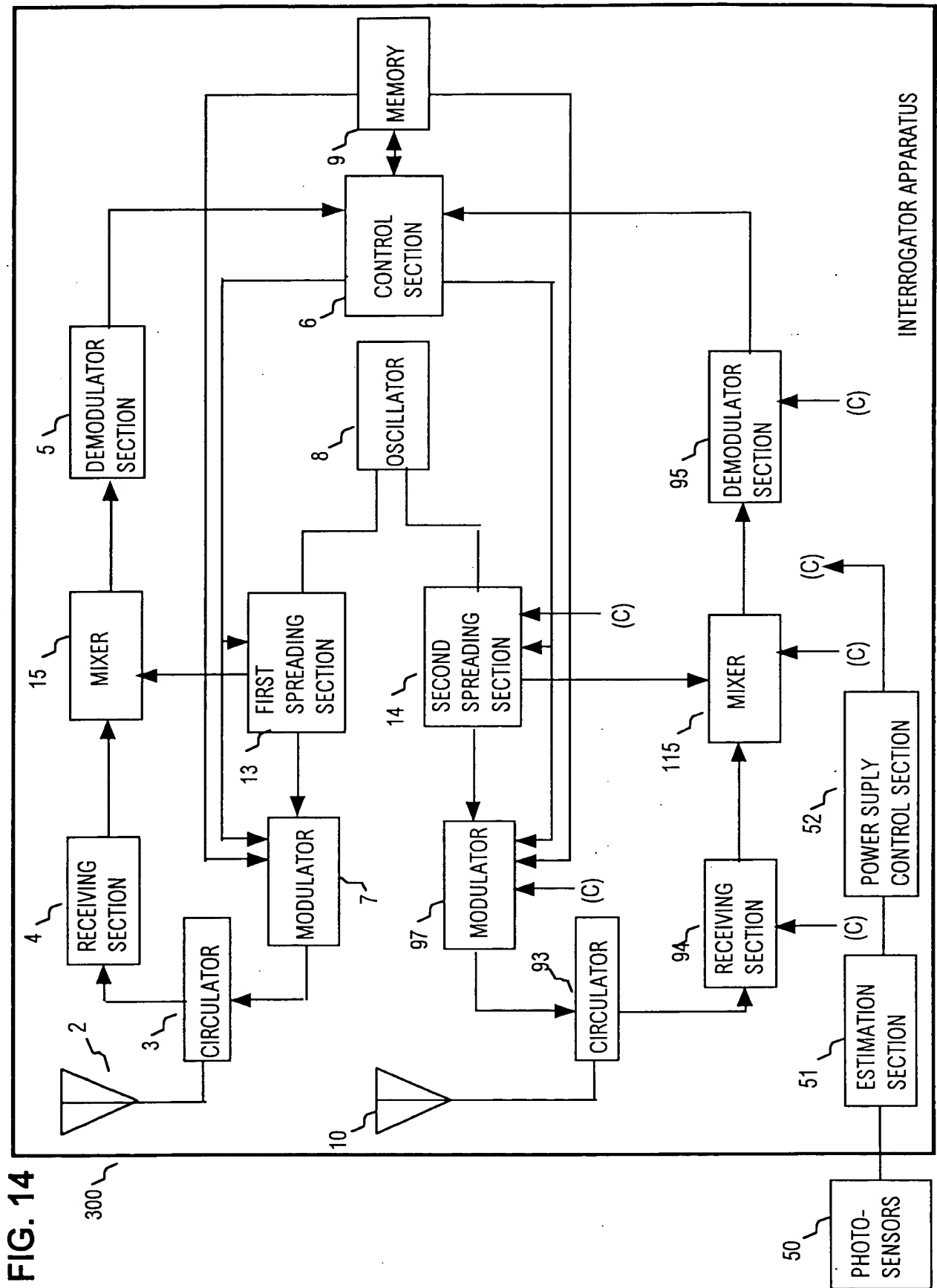


FIG. 15

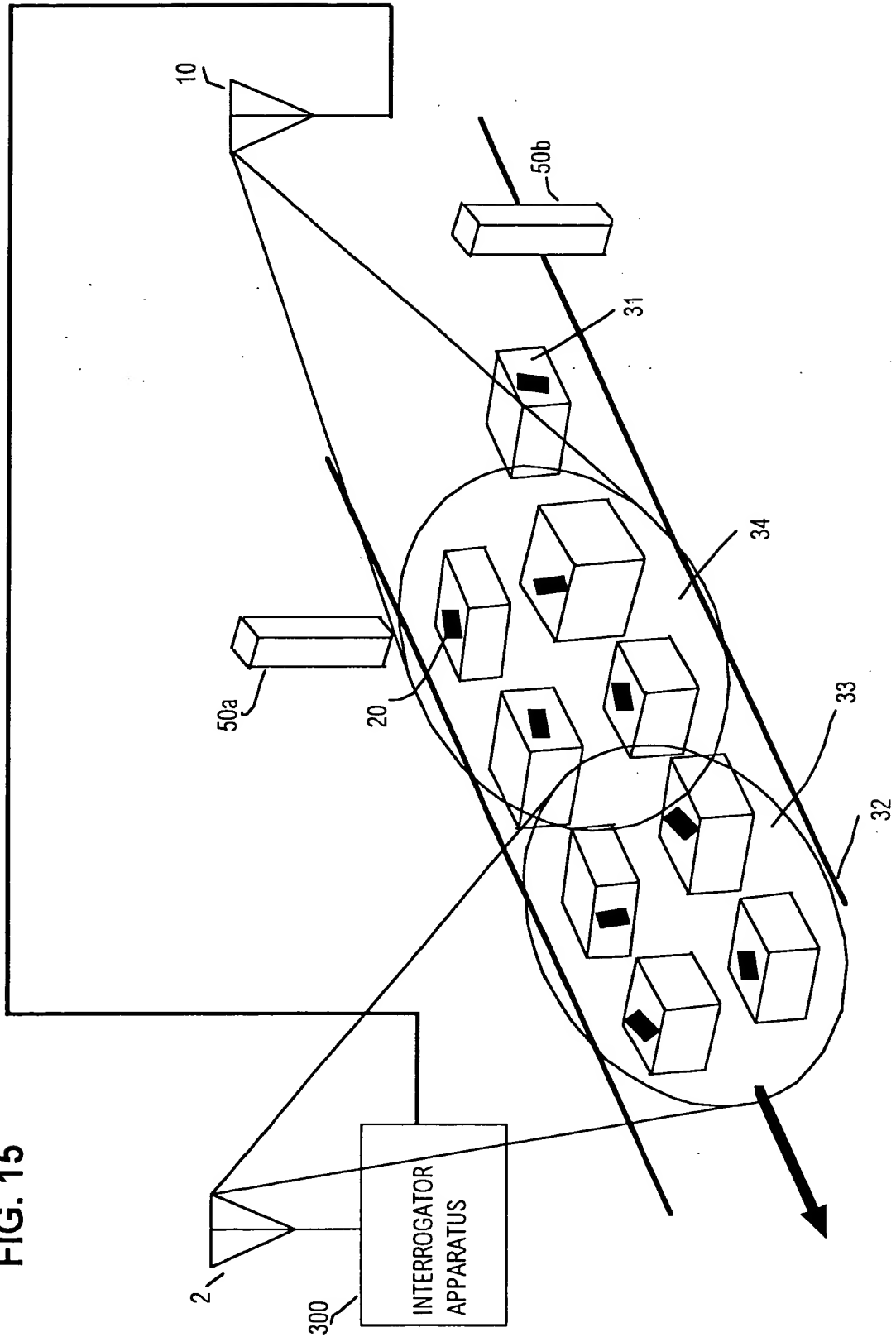


FIG. 16

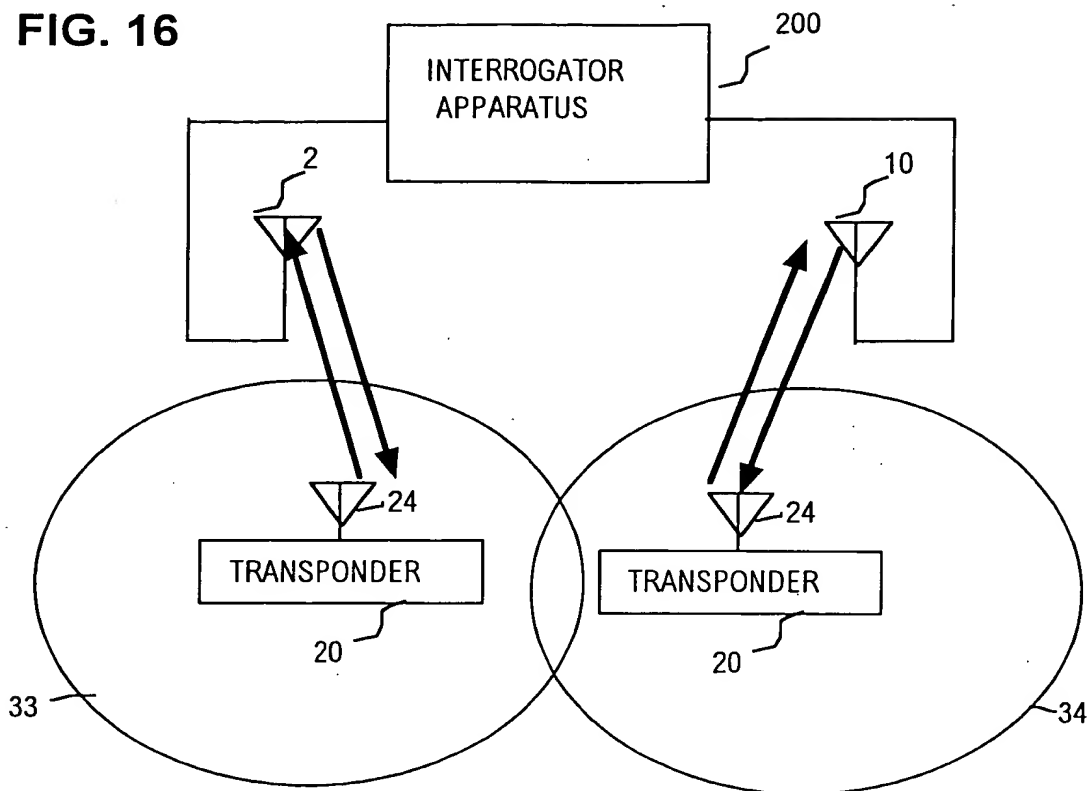


FIG. 17A

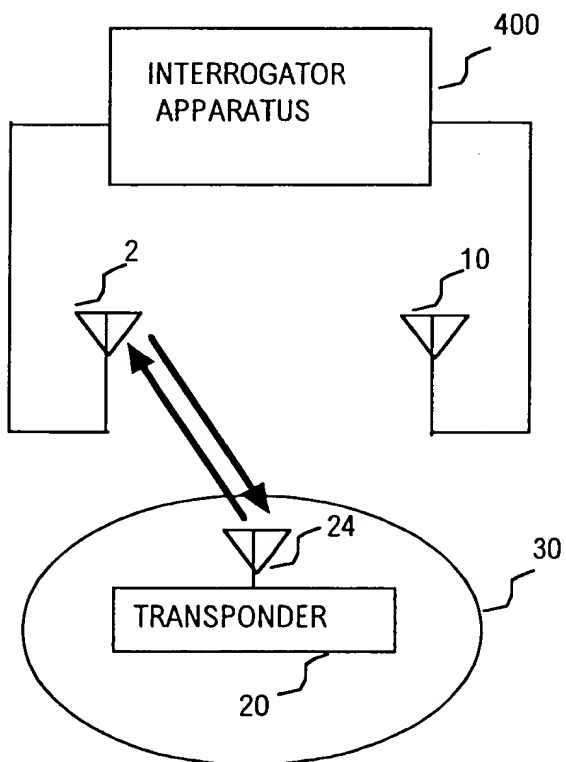
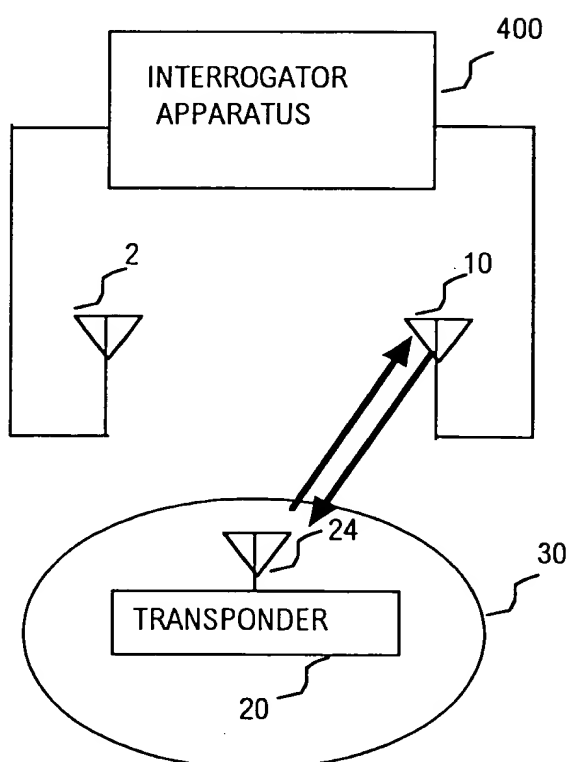


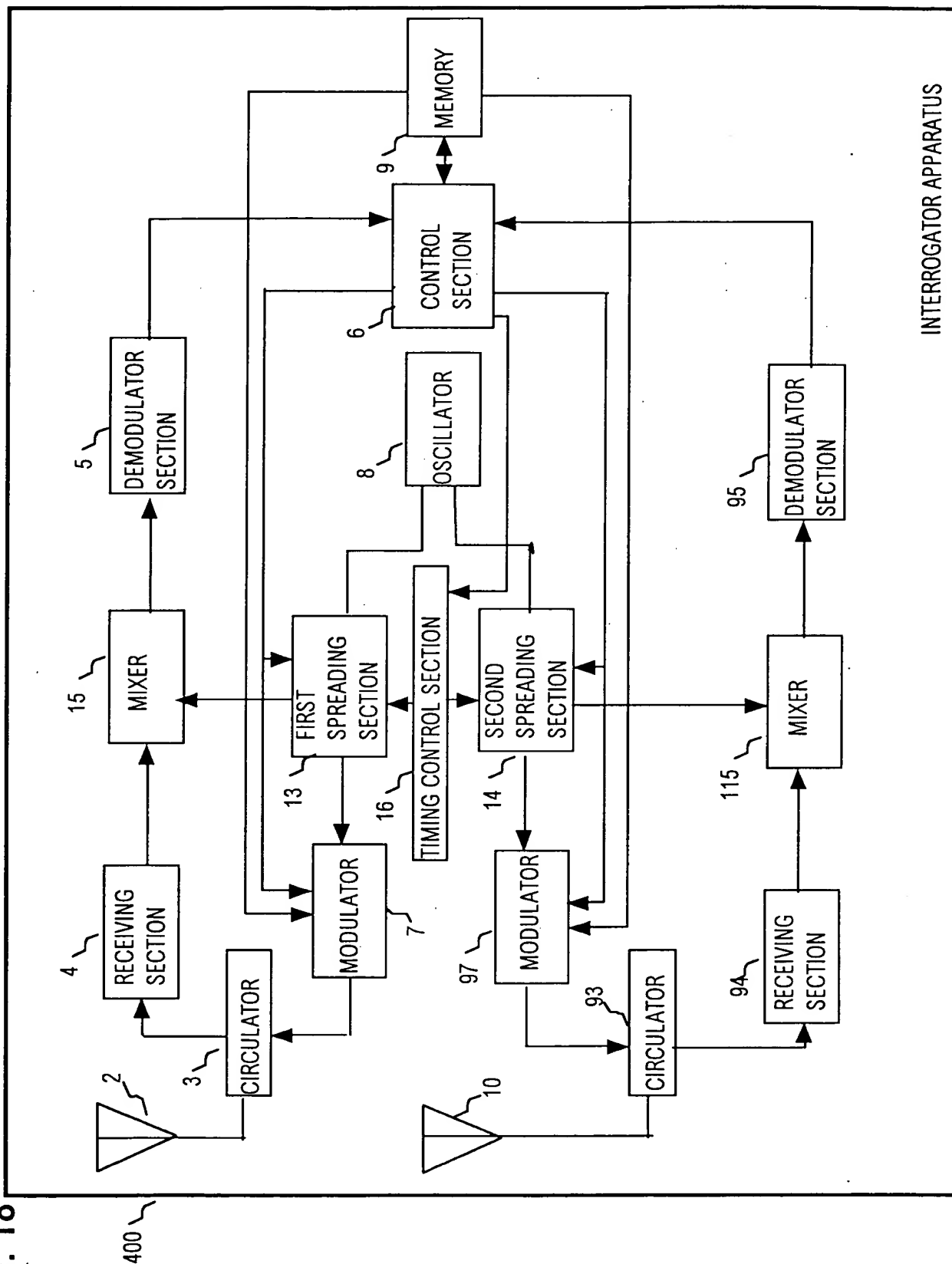
FIG. 17B



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FIG. 18

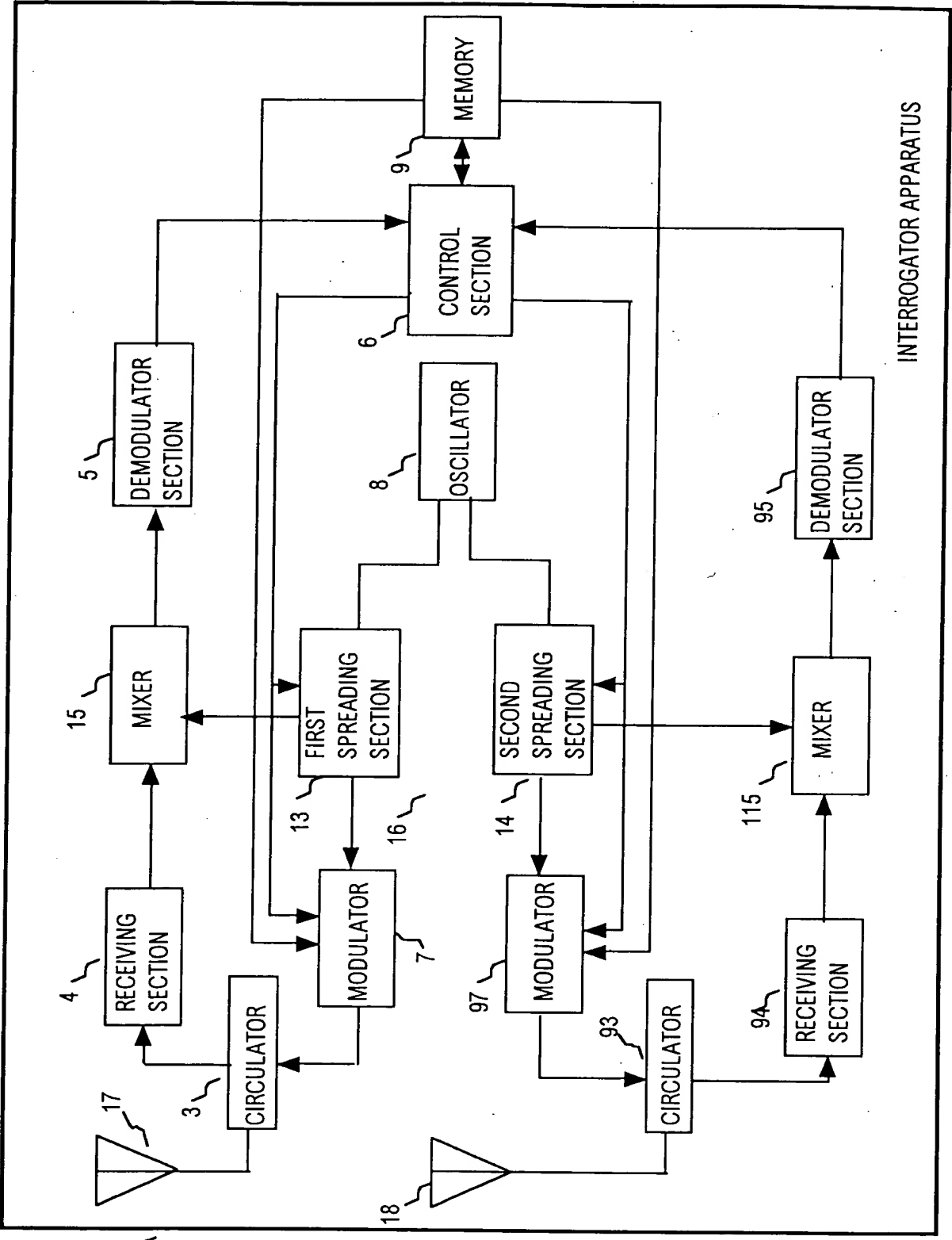


INTERROGATOR APPARATUS



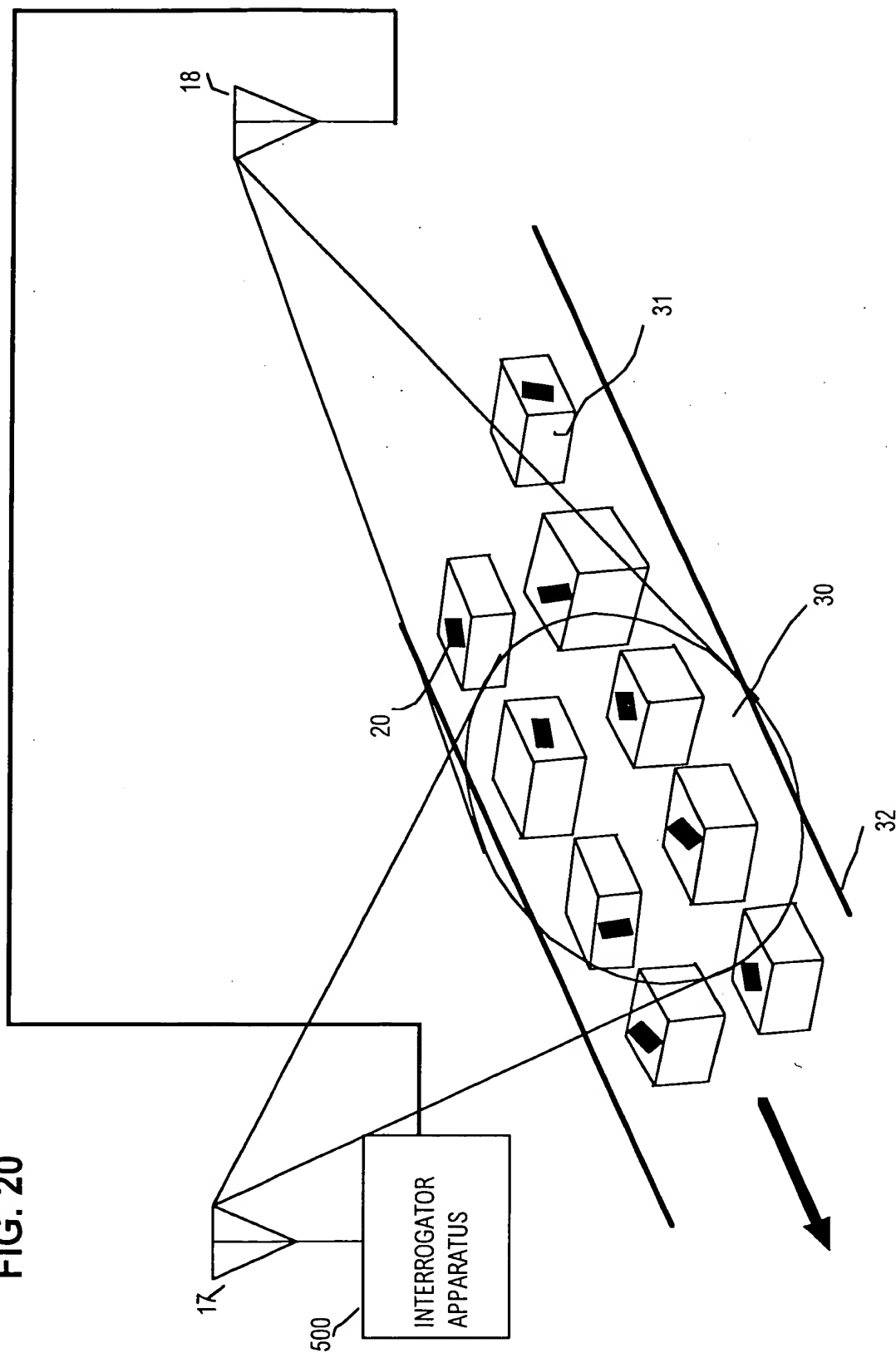
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FIG. 19



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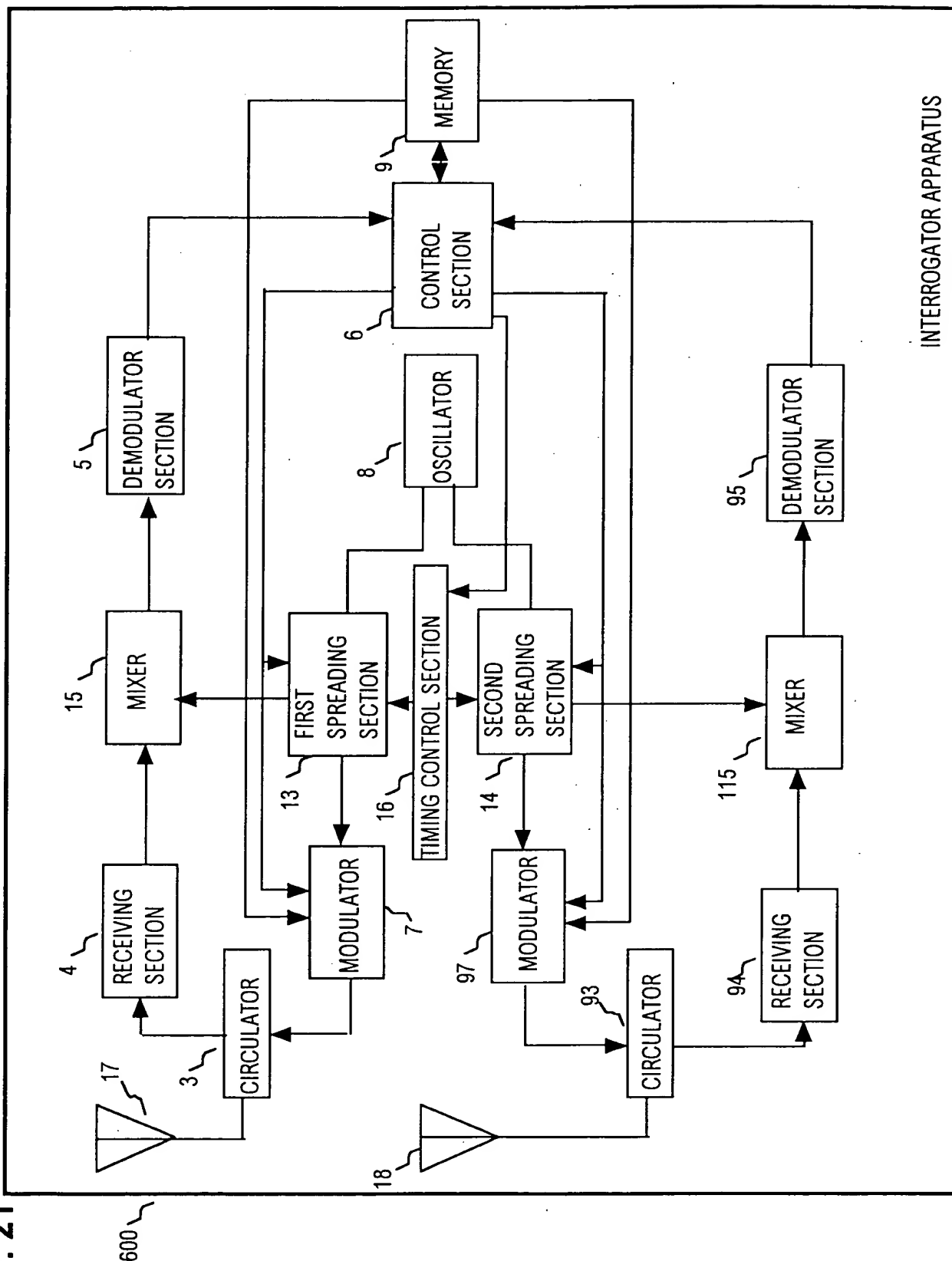
FIG. 20



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FIG. 21



INTERROGATOR APPARATUS

FIG. 22

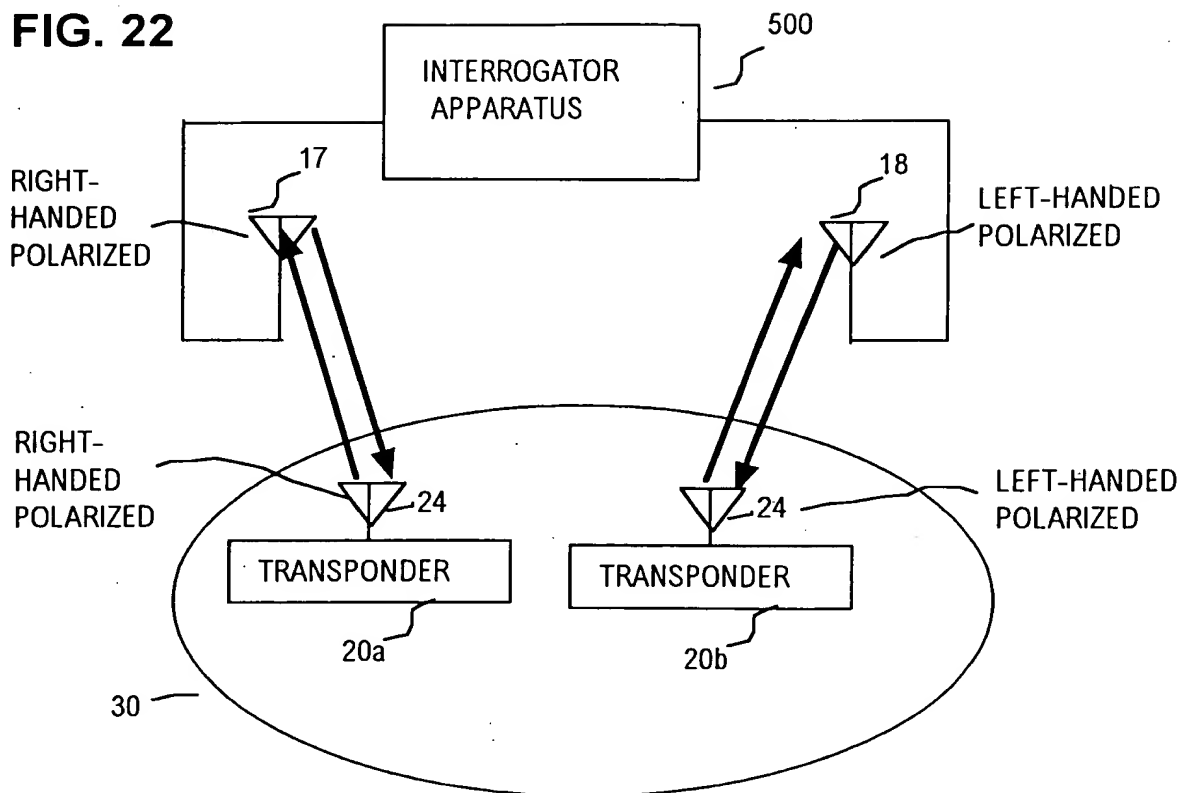
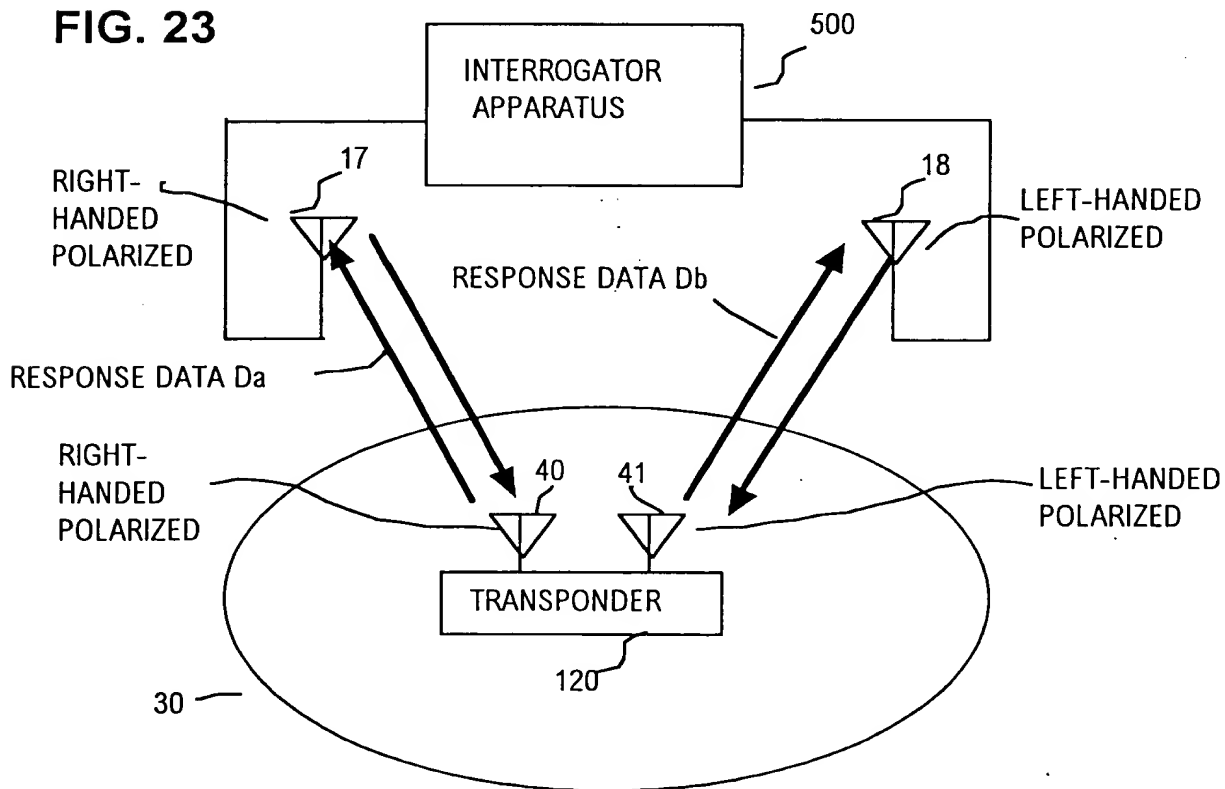


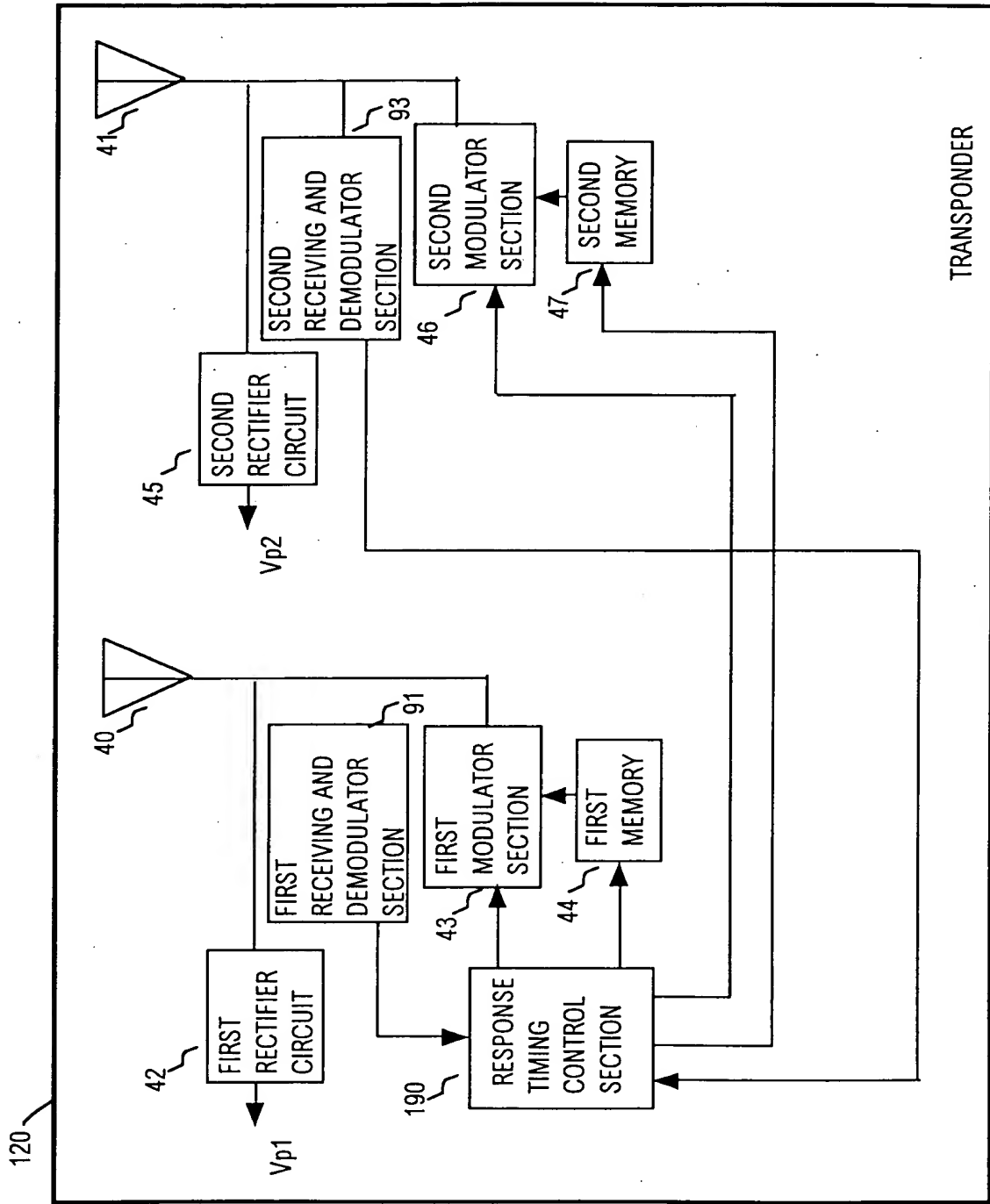
FIG. 23



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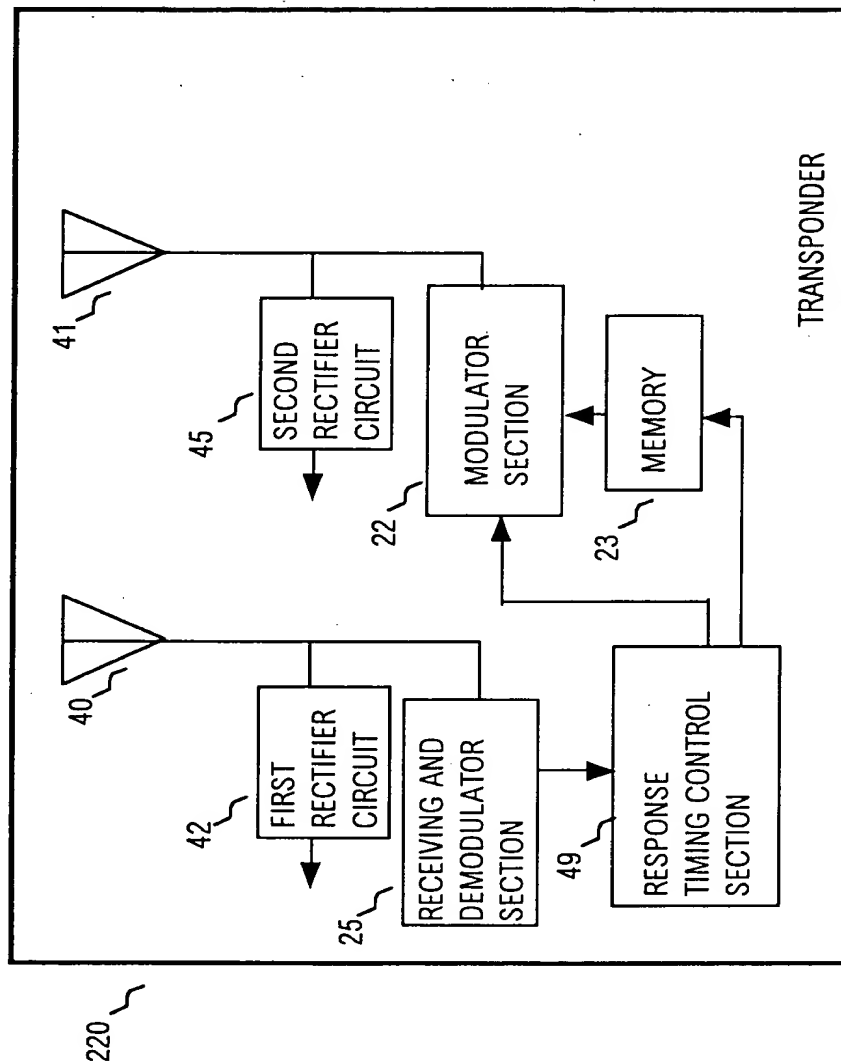
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FIG. 24



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FIG. 25



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FIG. 26

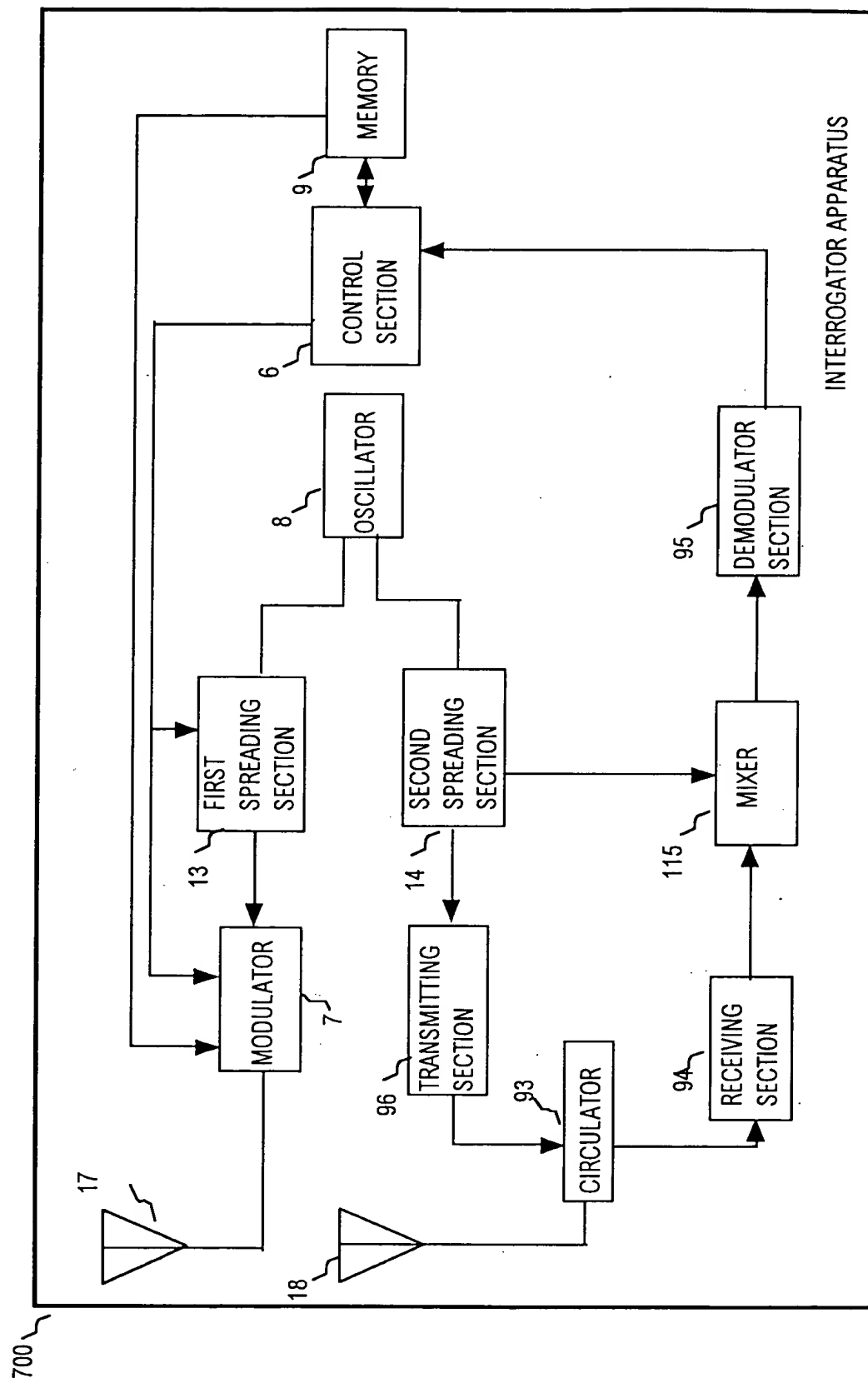


FIG. 27

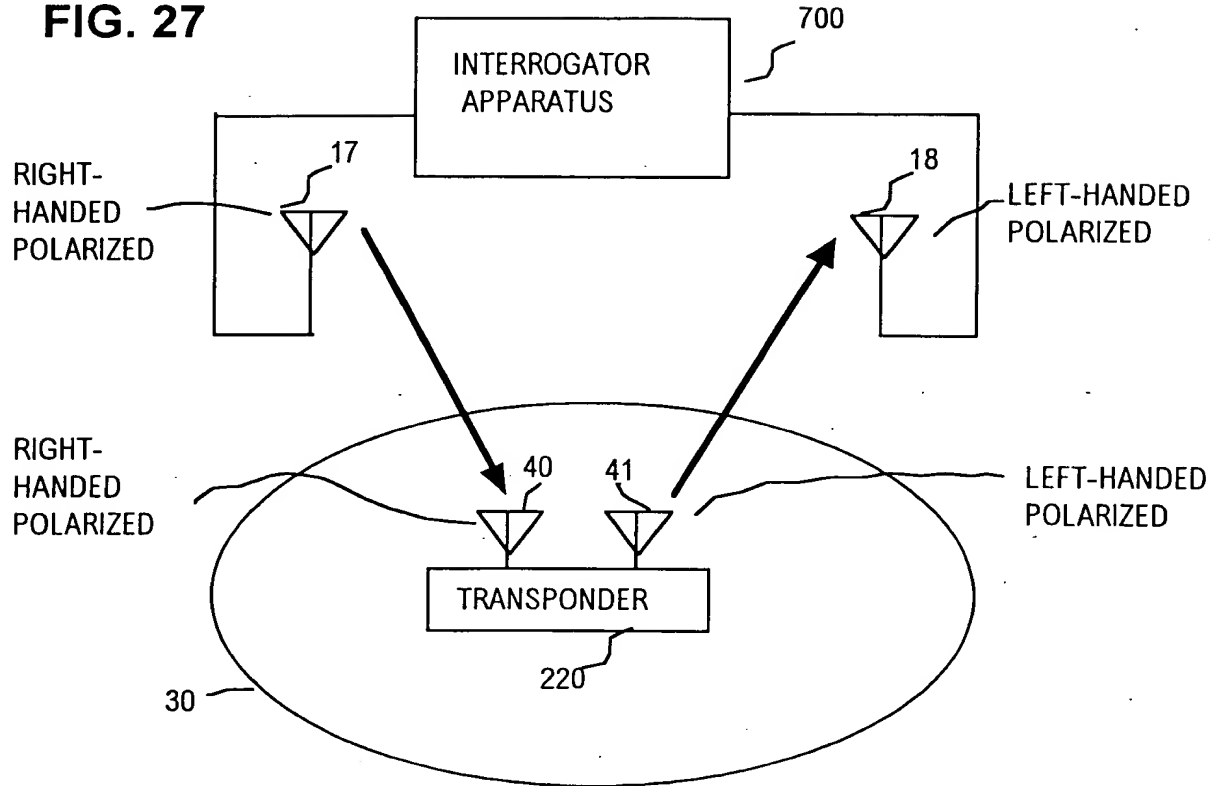


FIG. 28

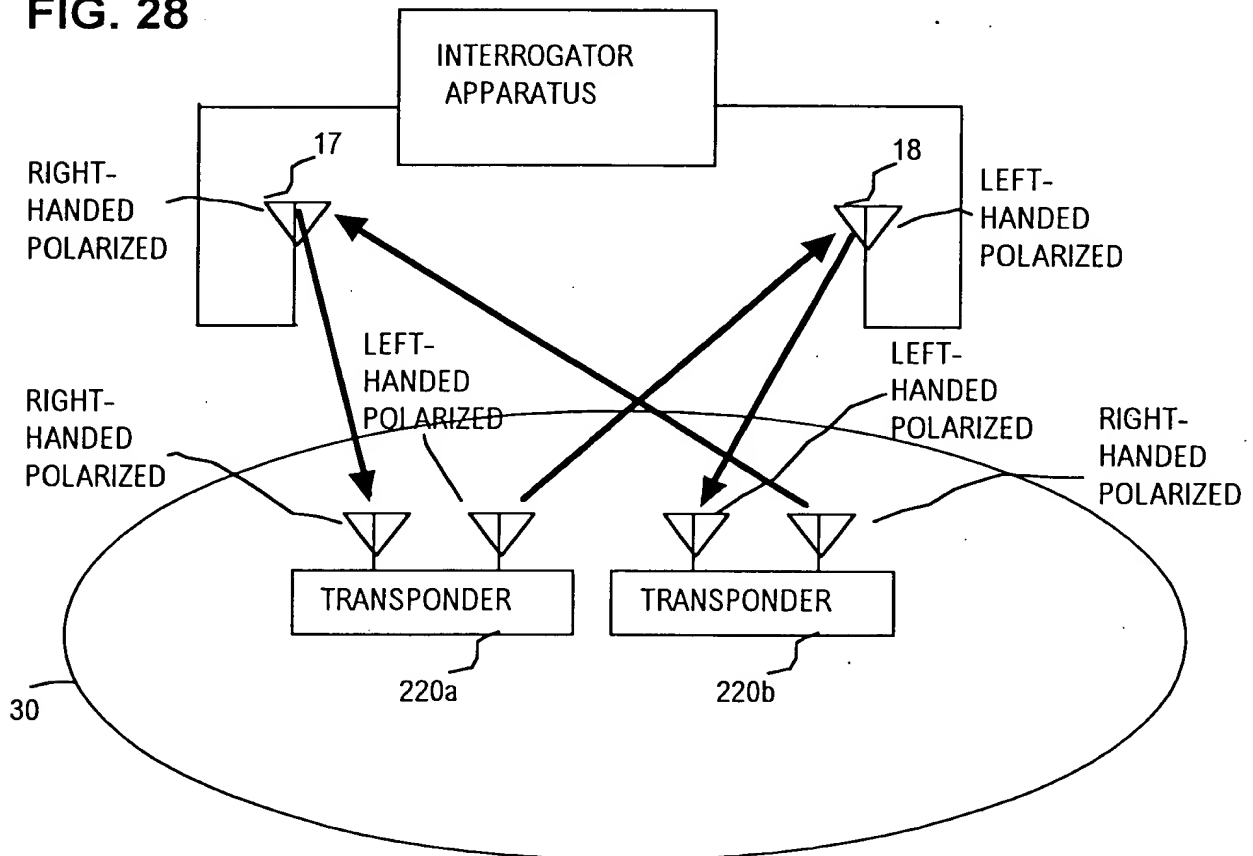




FIG. 29

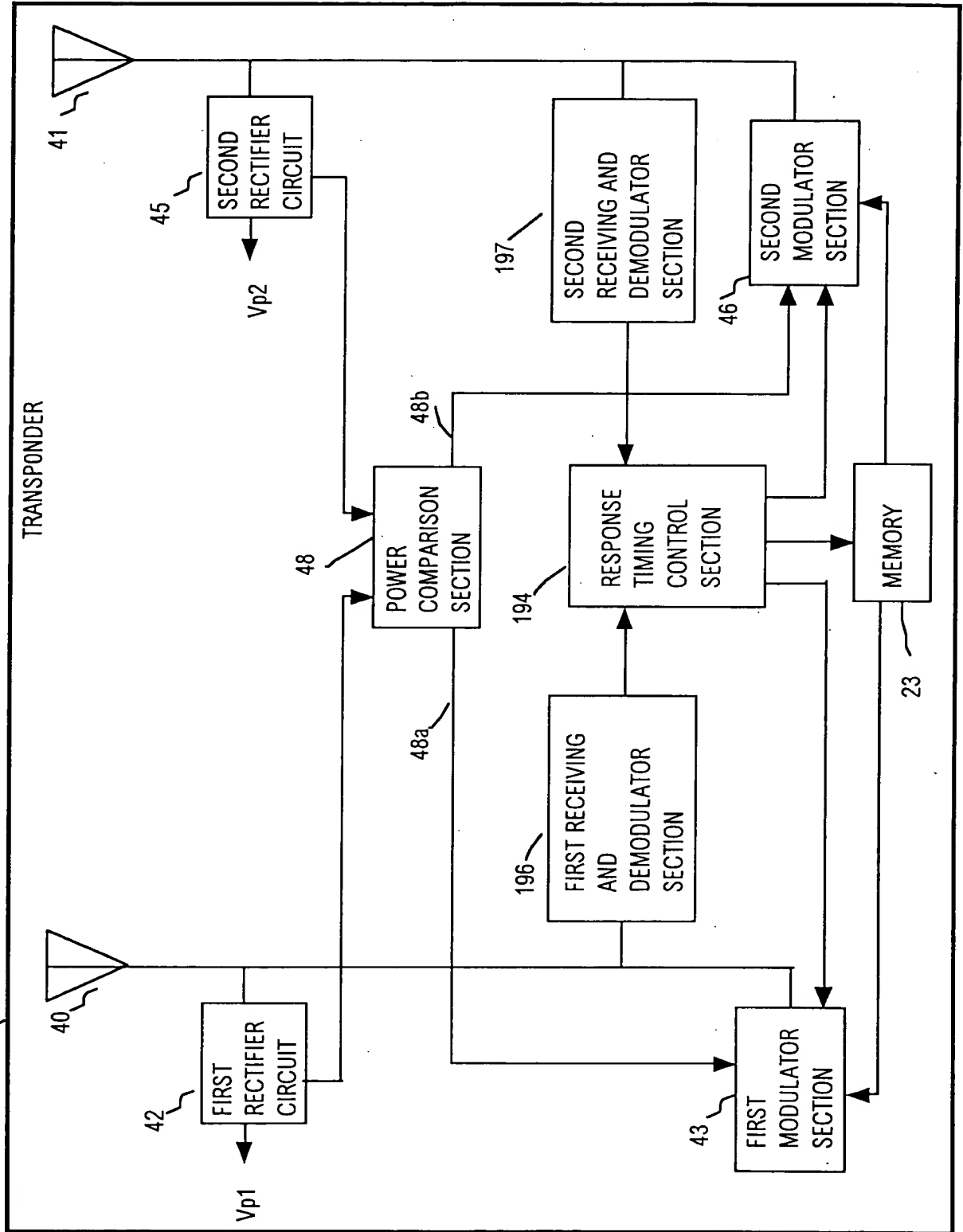


FIG. 30A

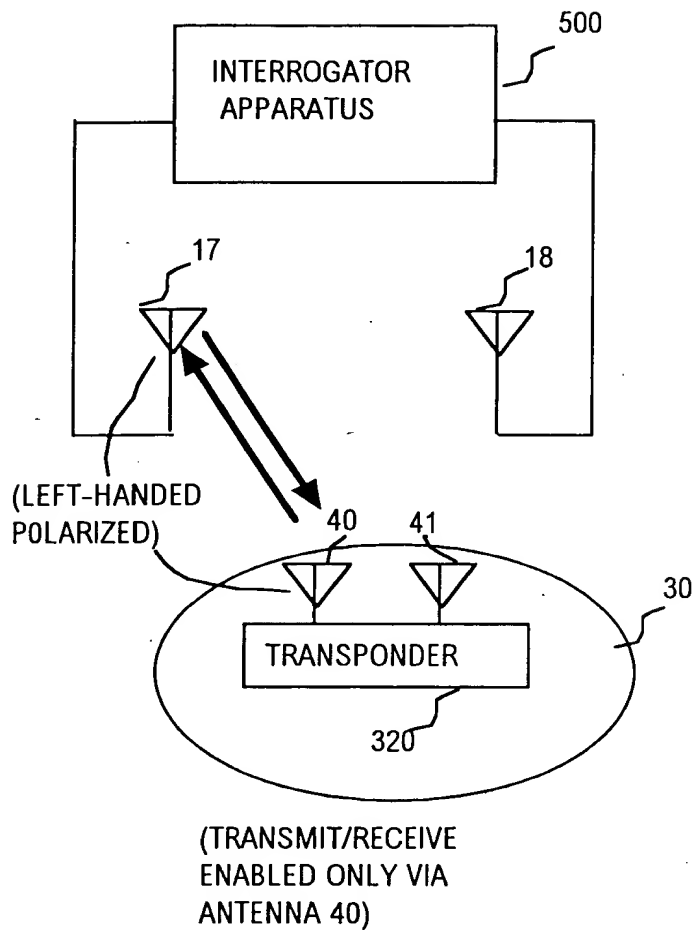


FIG. 30B

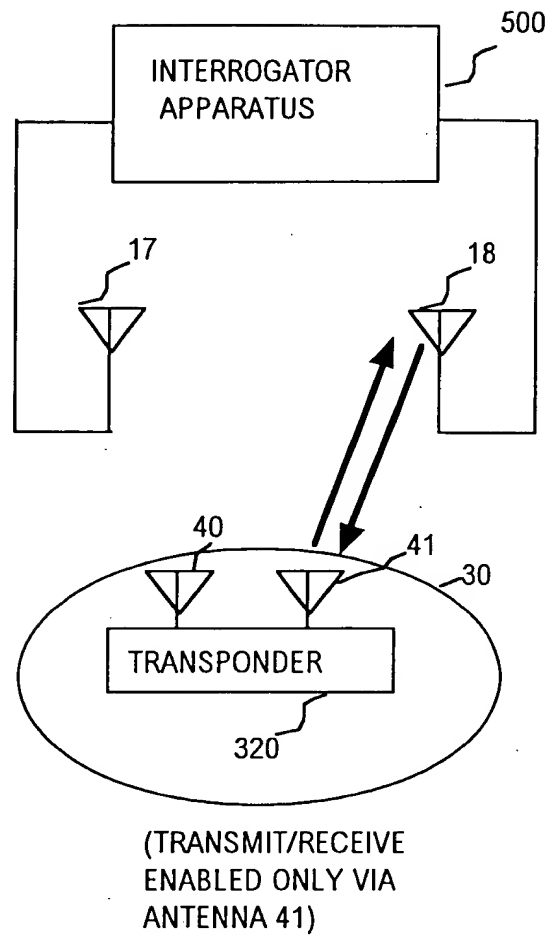
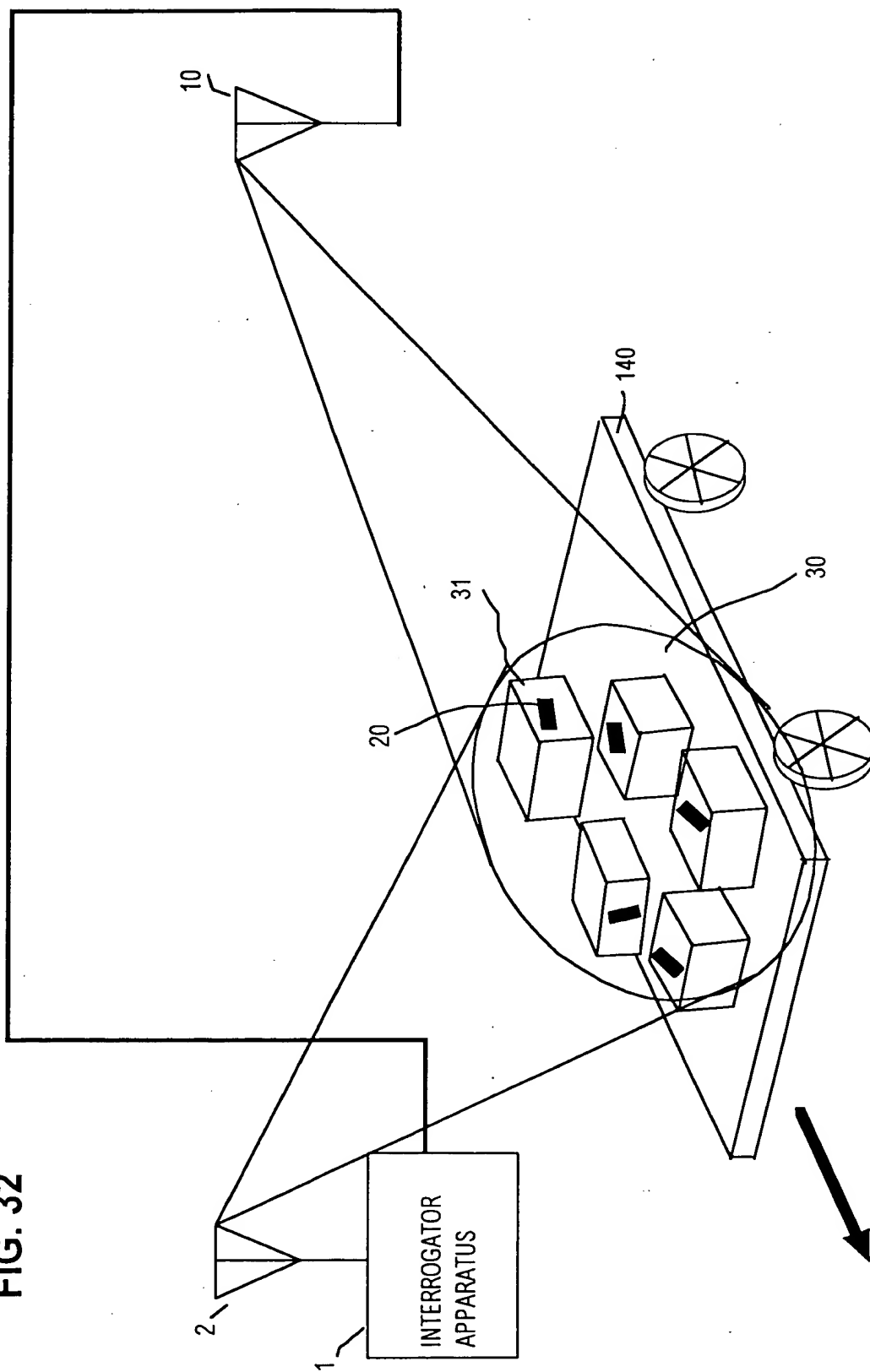




FIG. 32



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**FIG. 33**

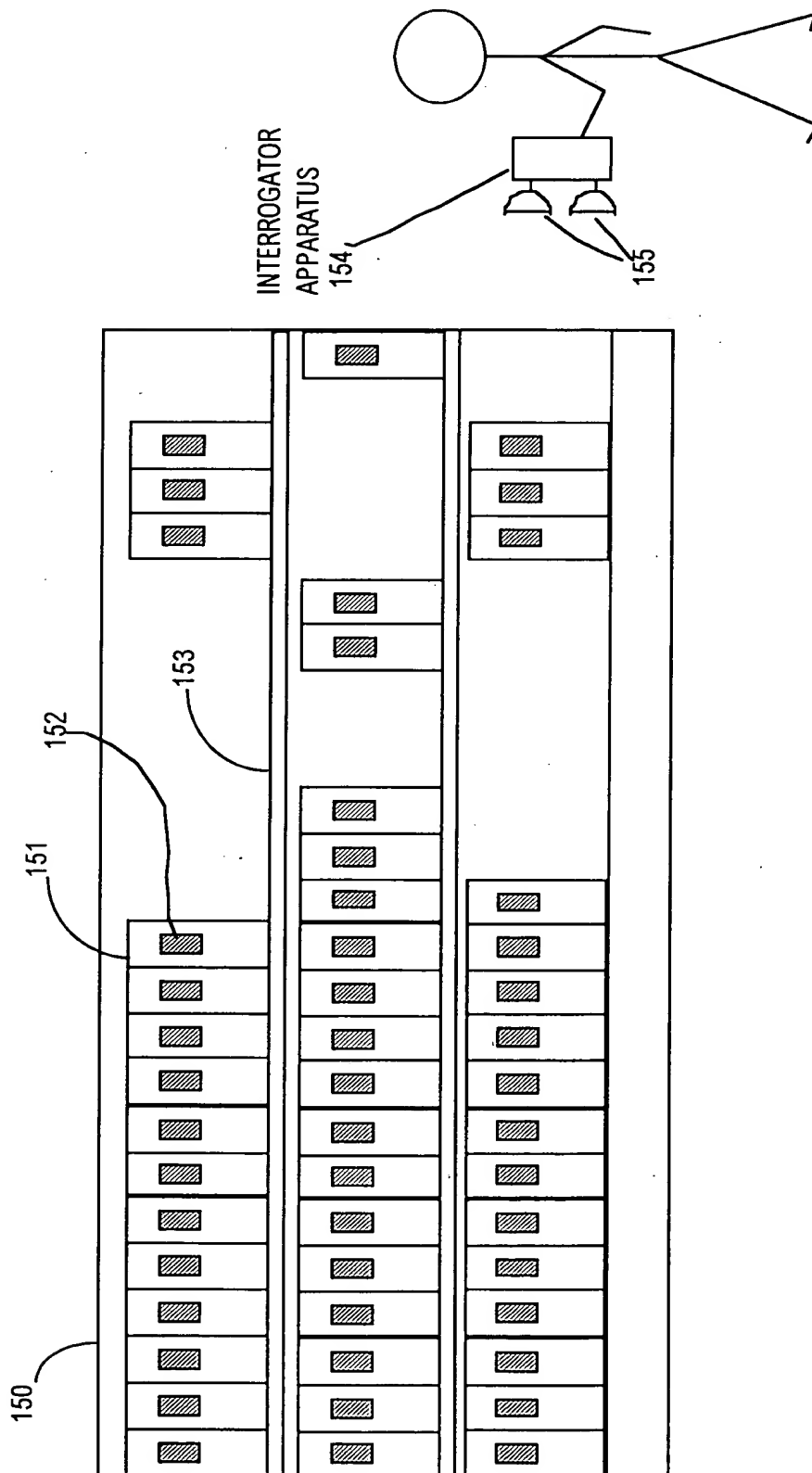
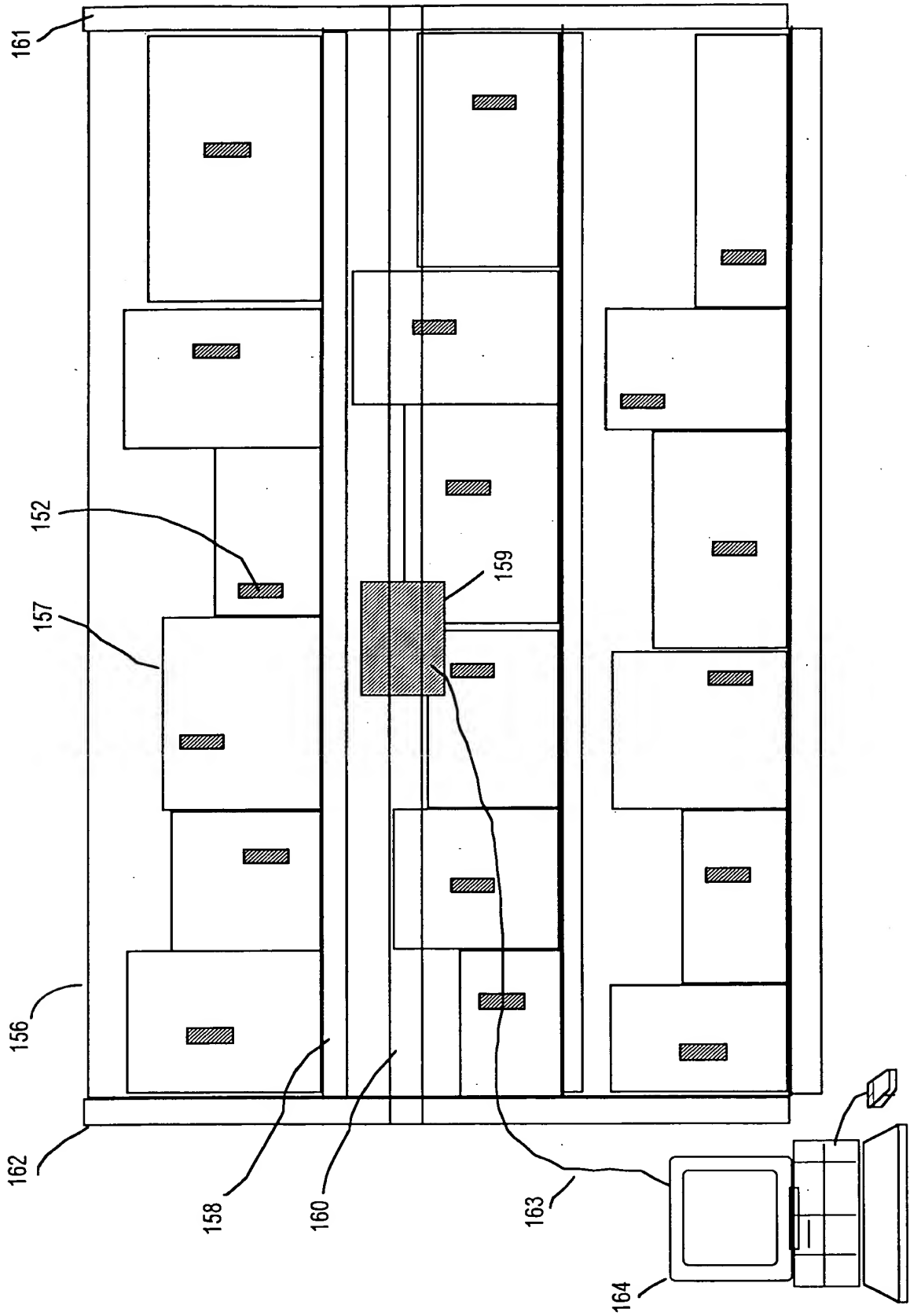
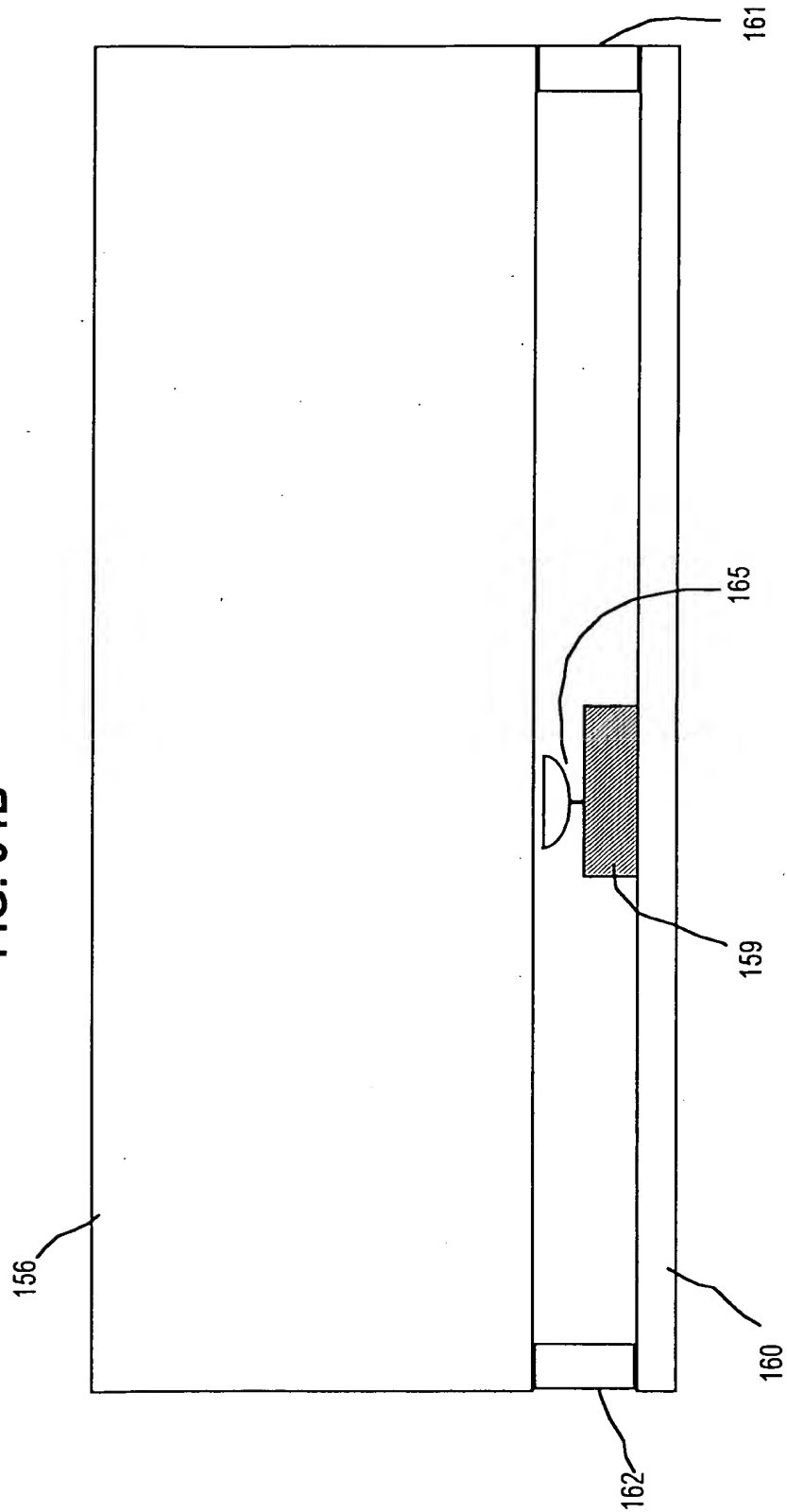


FIG. 34A



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FIG. 34B



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FIG. 35

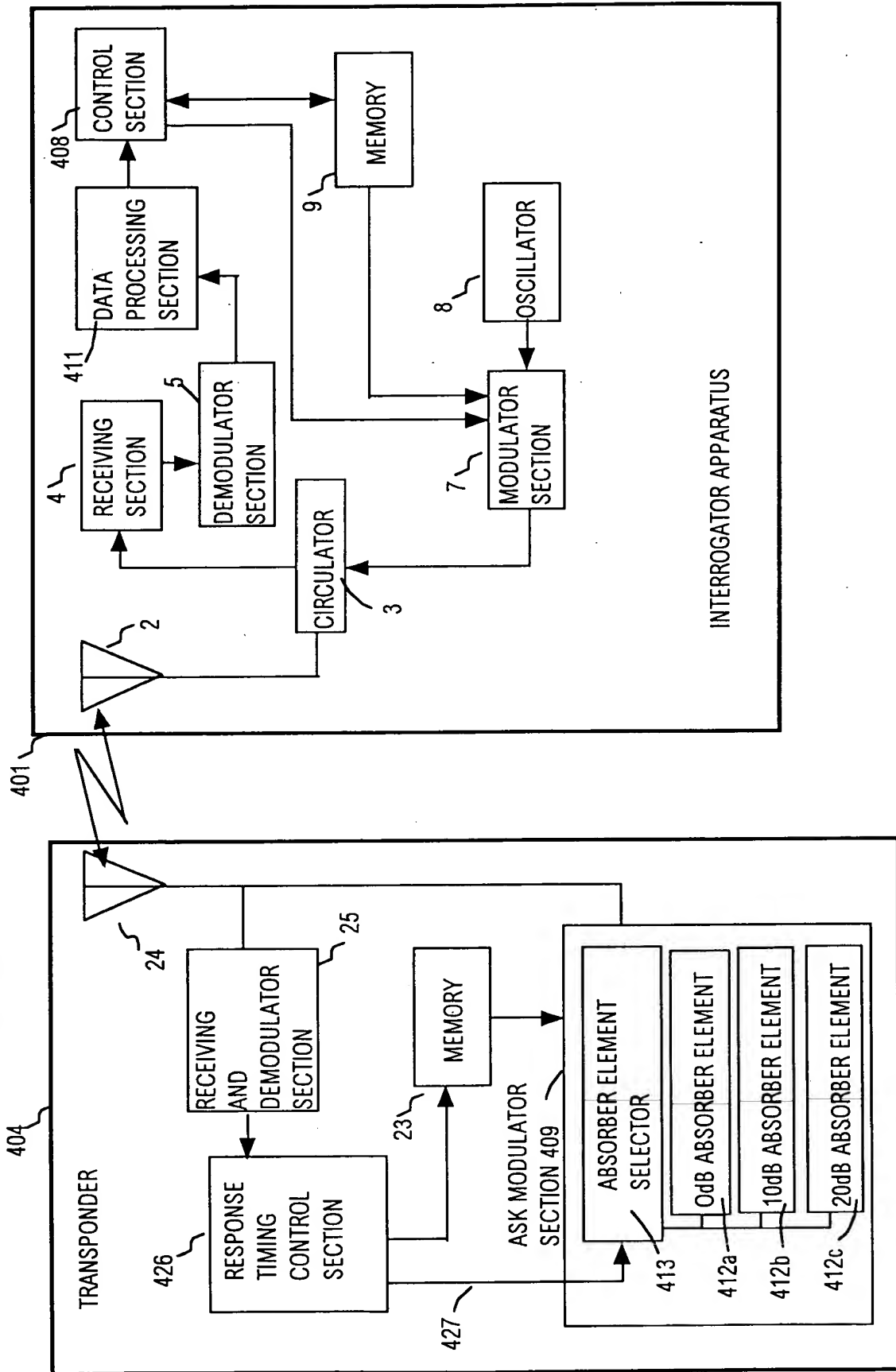
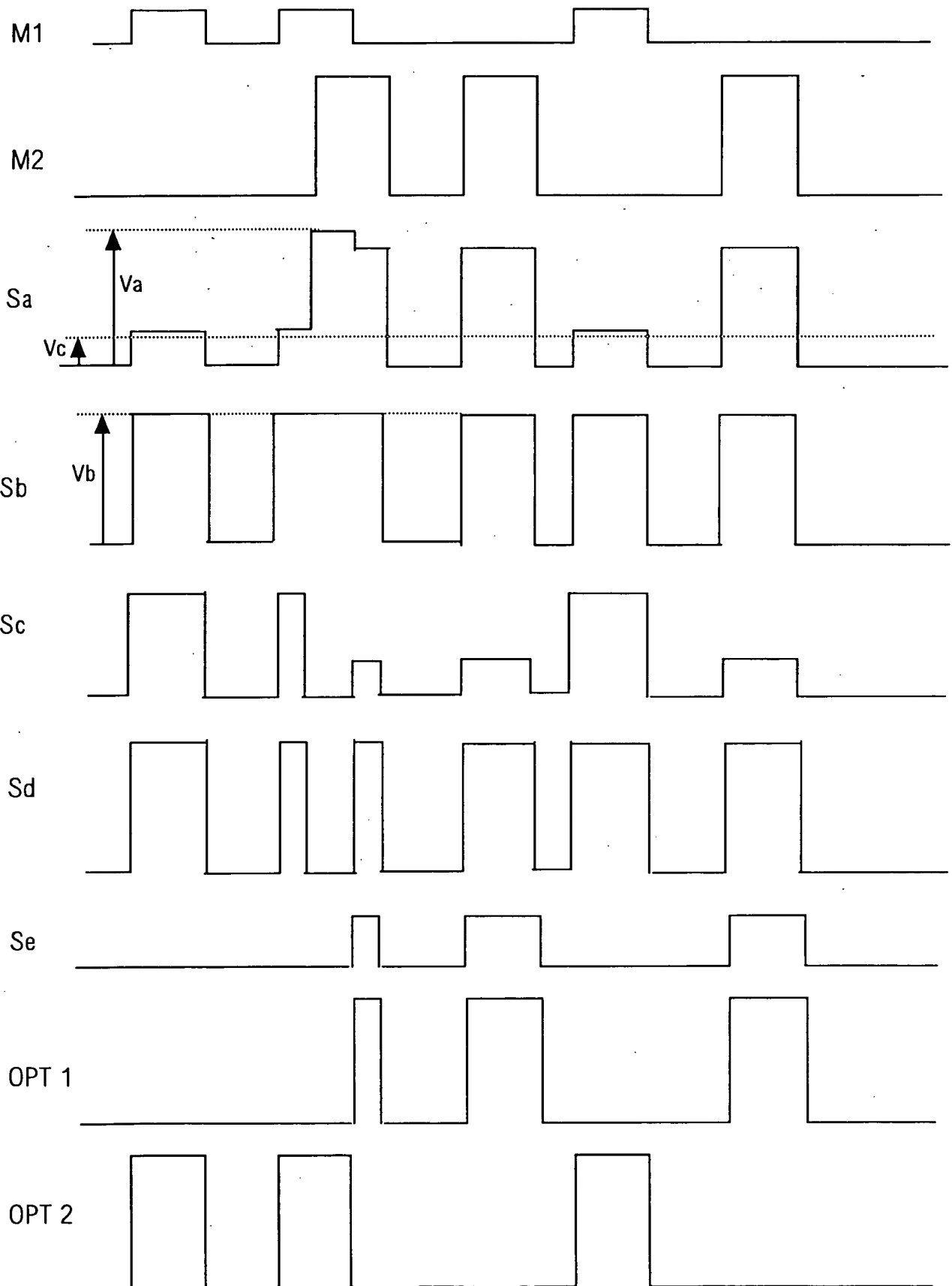




FIG. 36



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FIG. 37

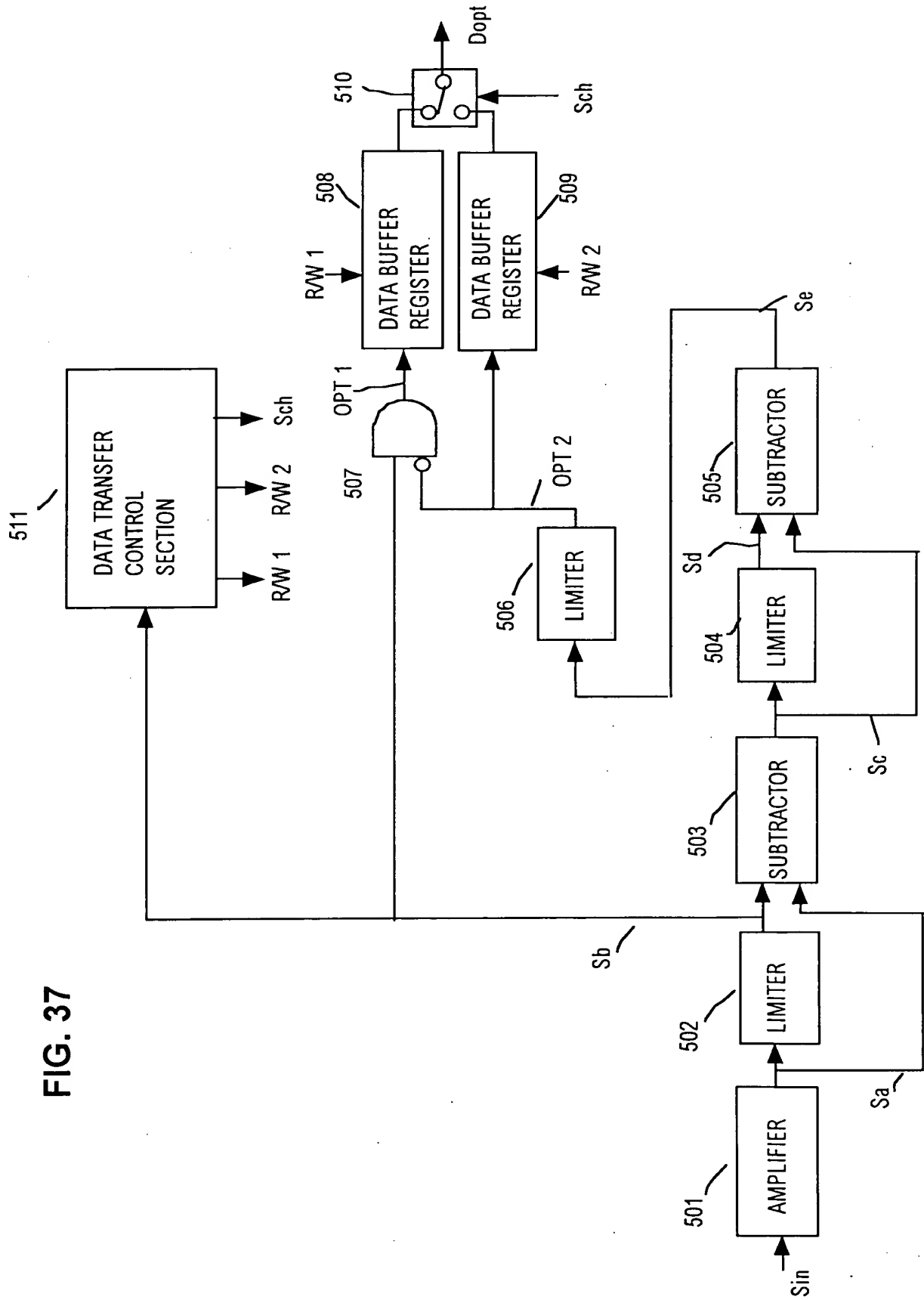


FIG. 38

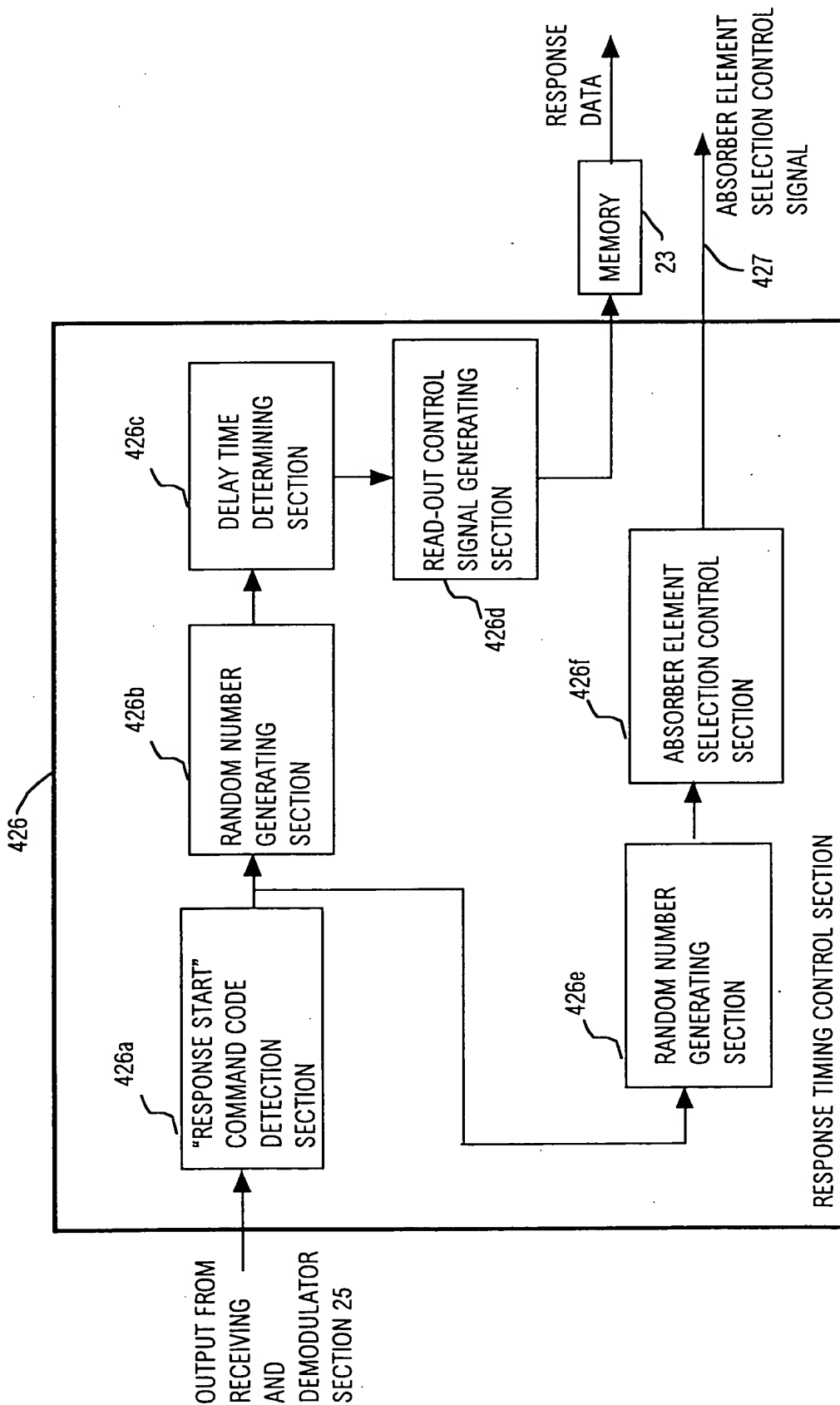


FIG. 39

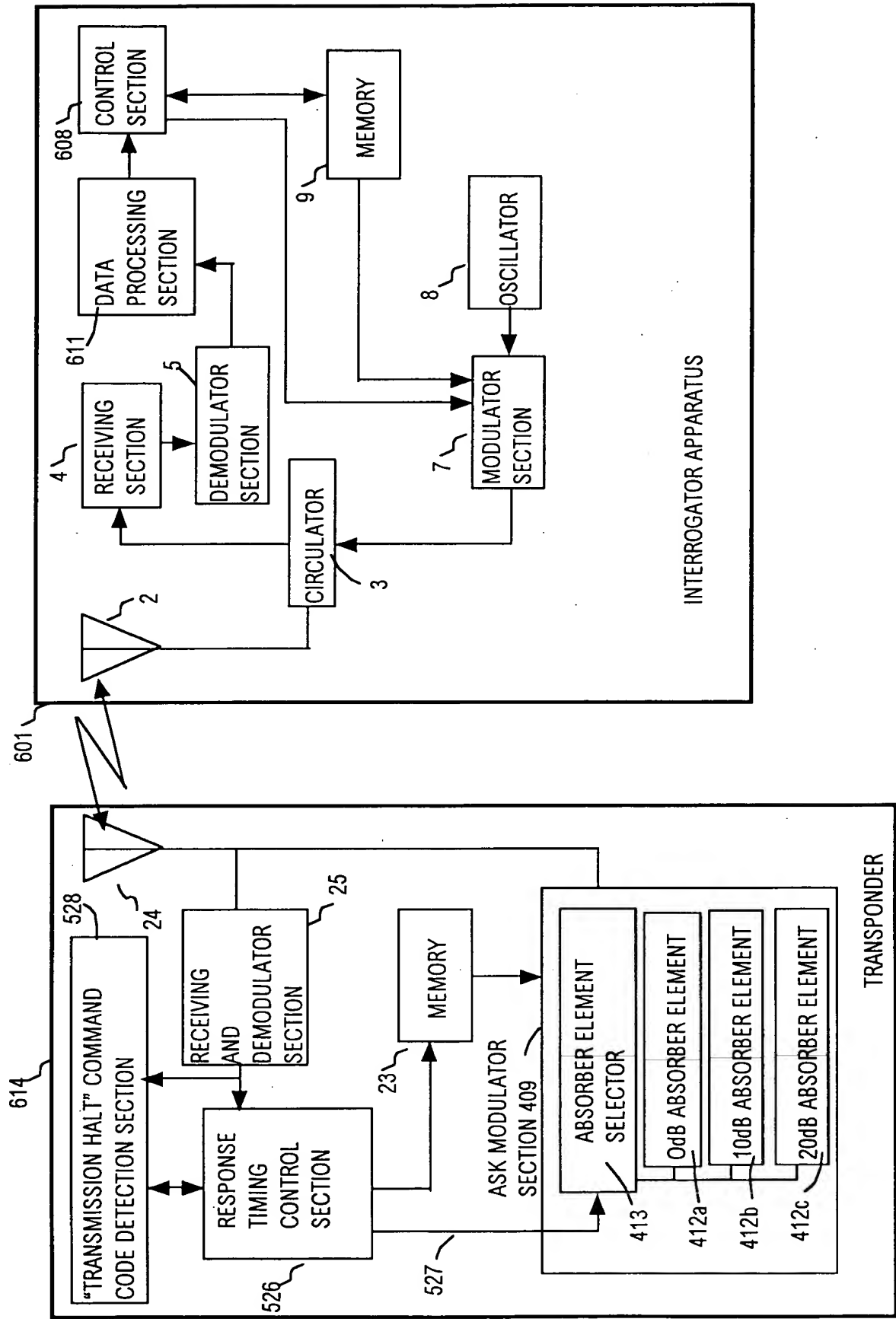


FIG. 40

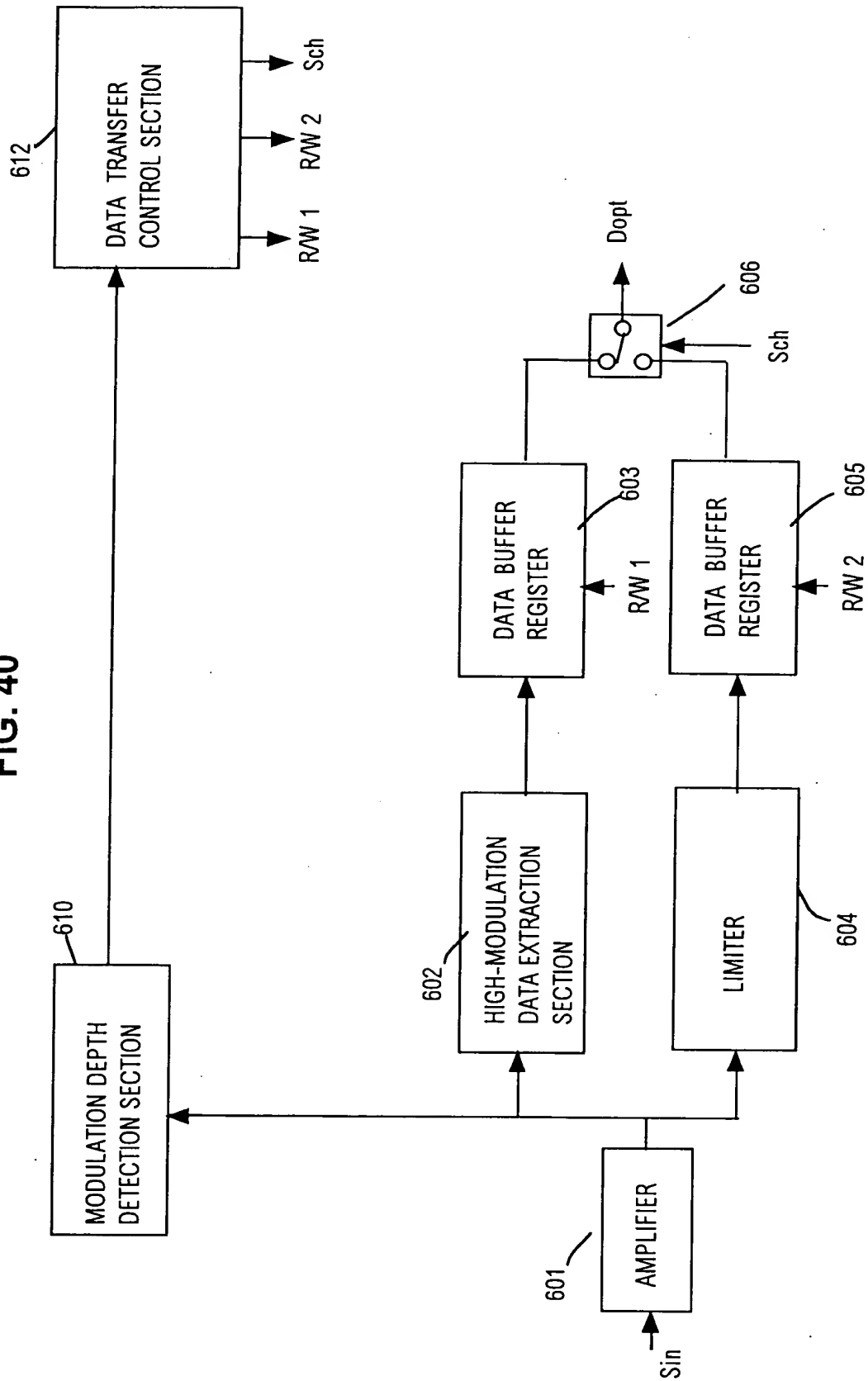
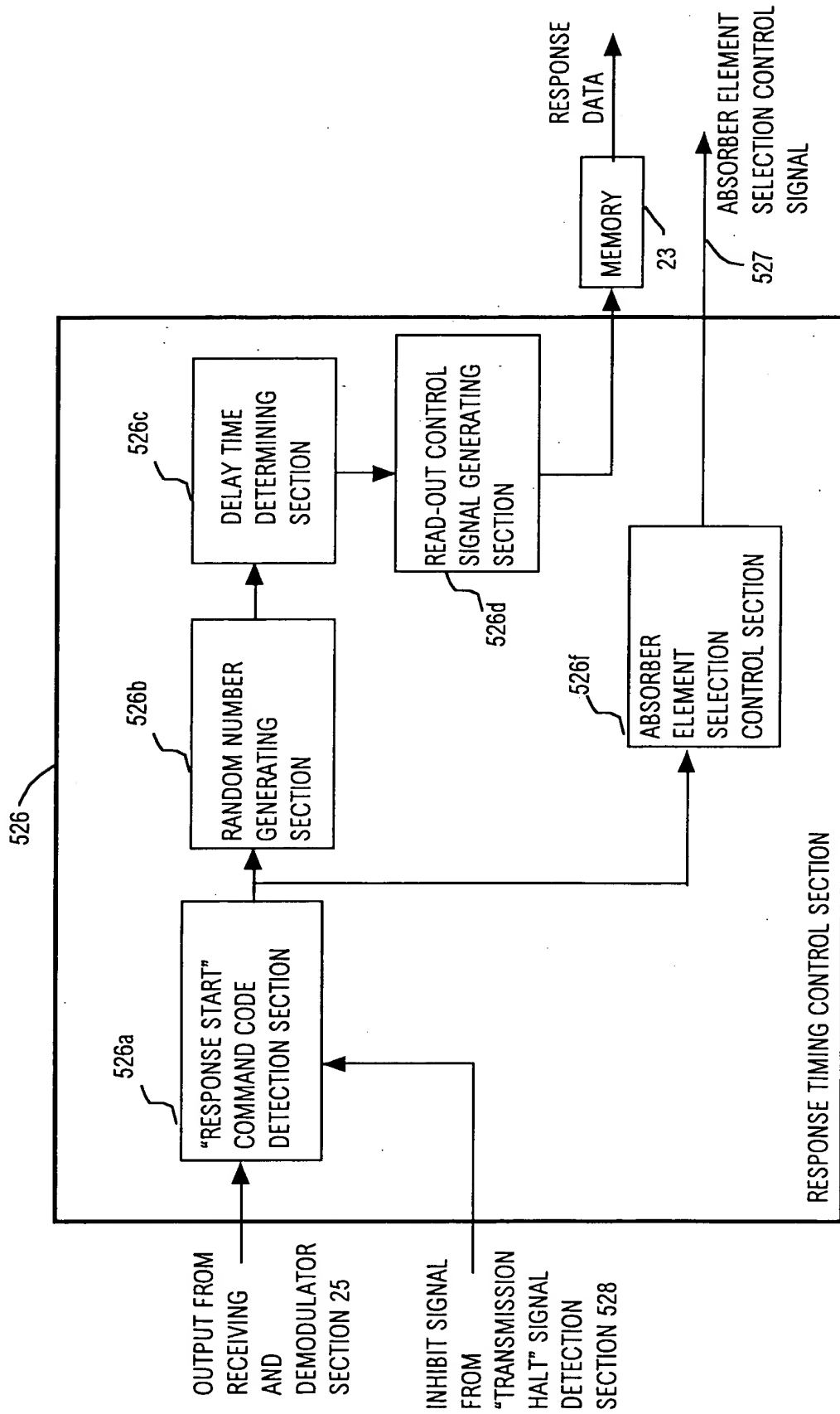


FIG. 41



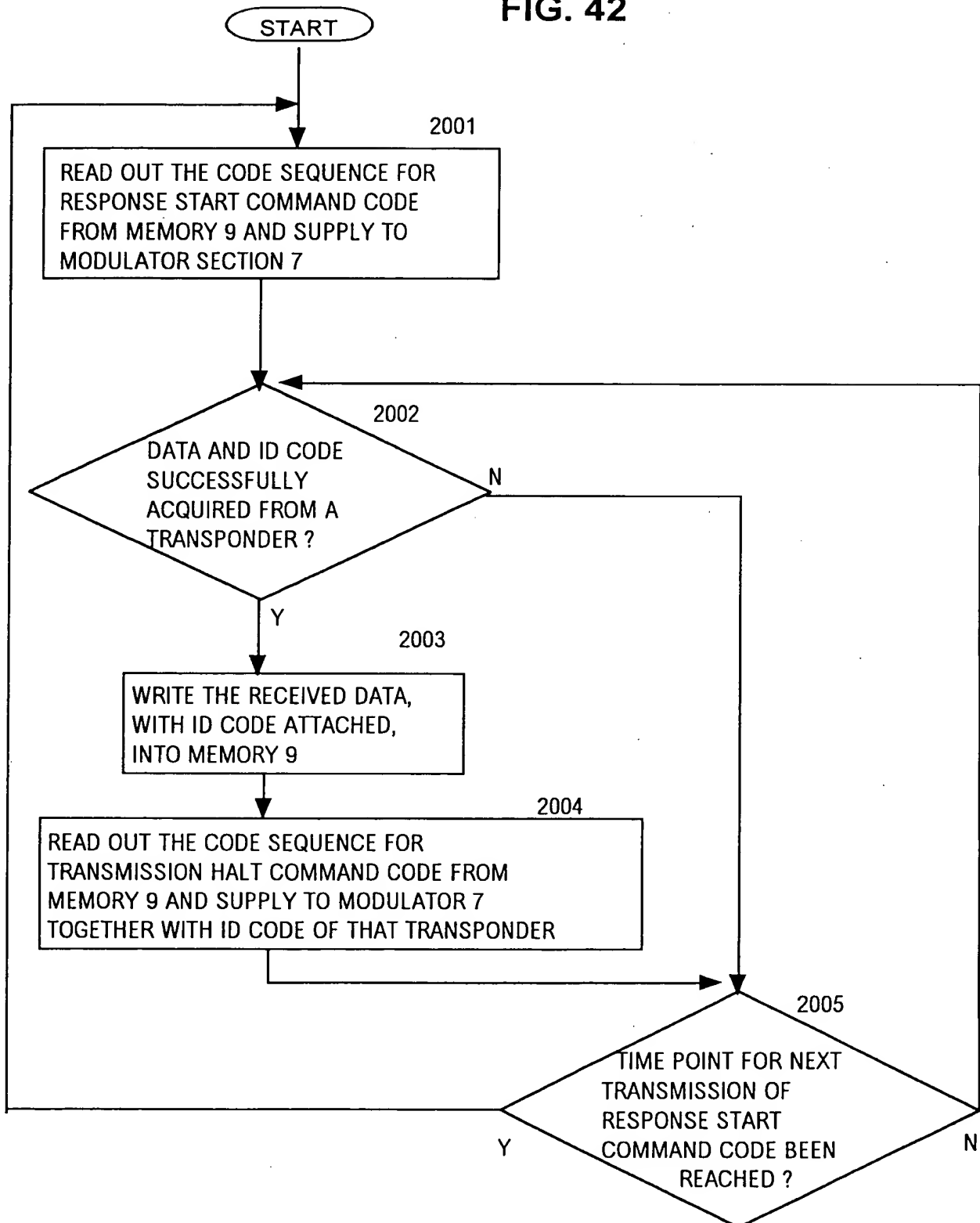
[illegible]

FIG. 43

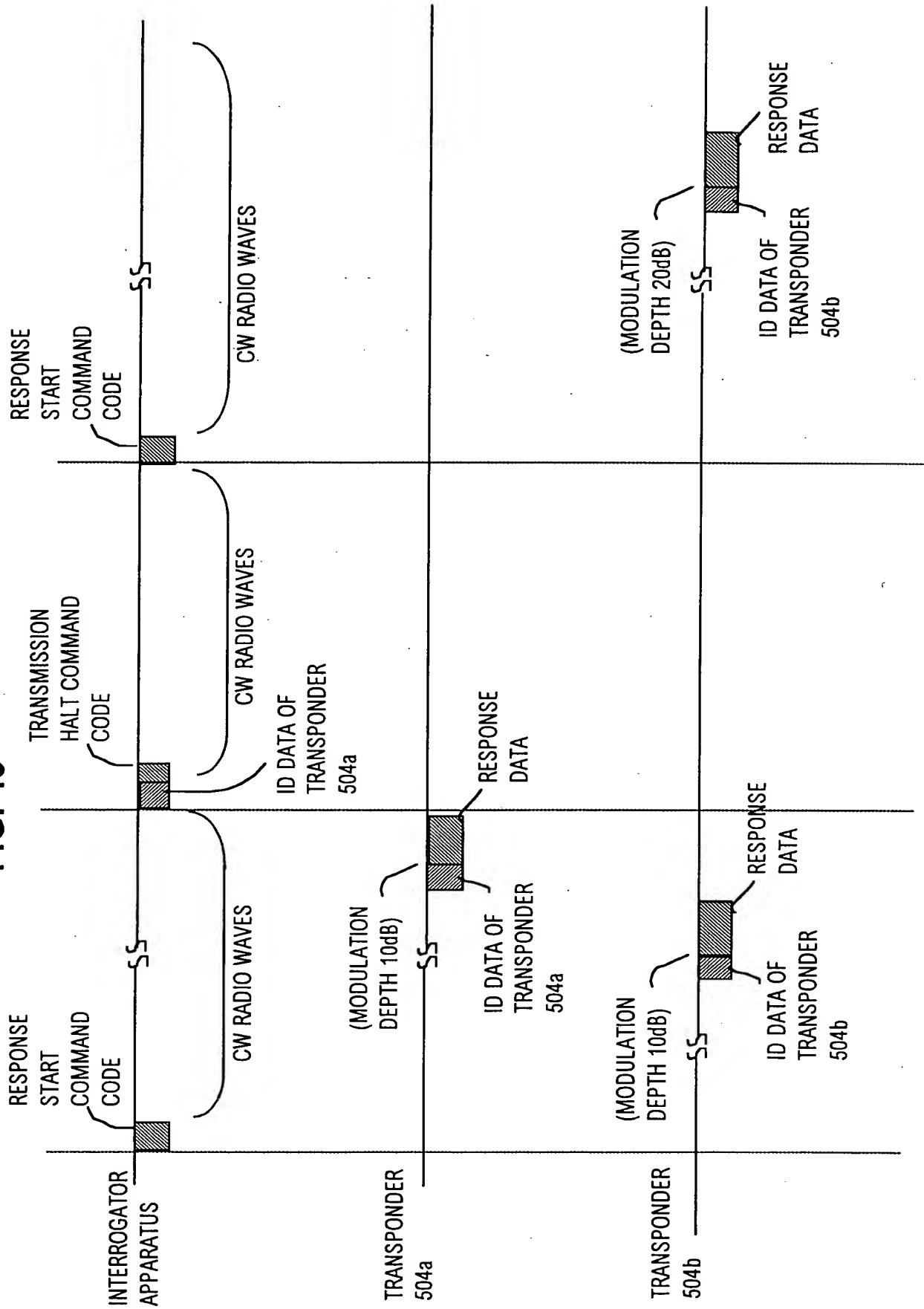




FIG. 44

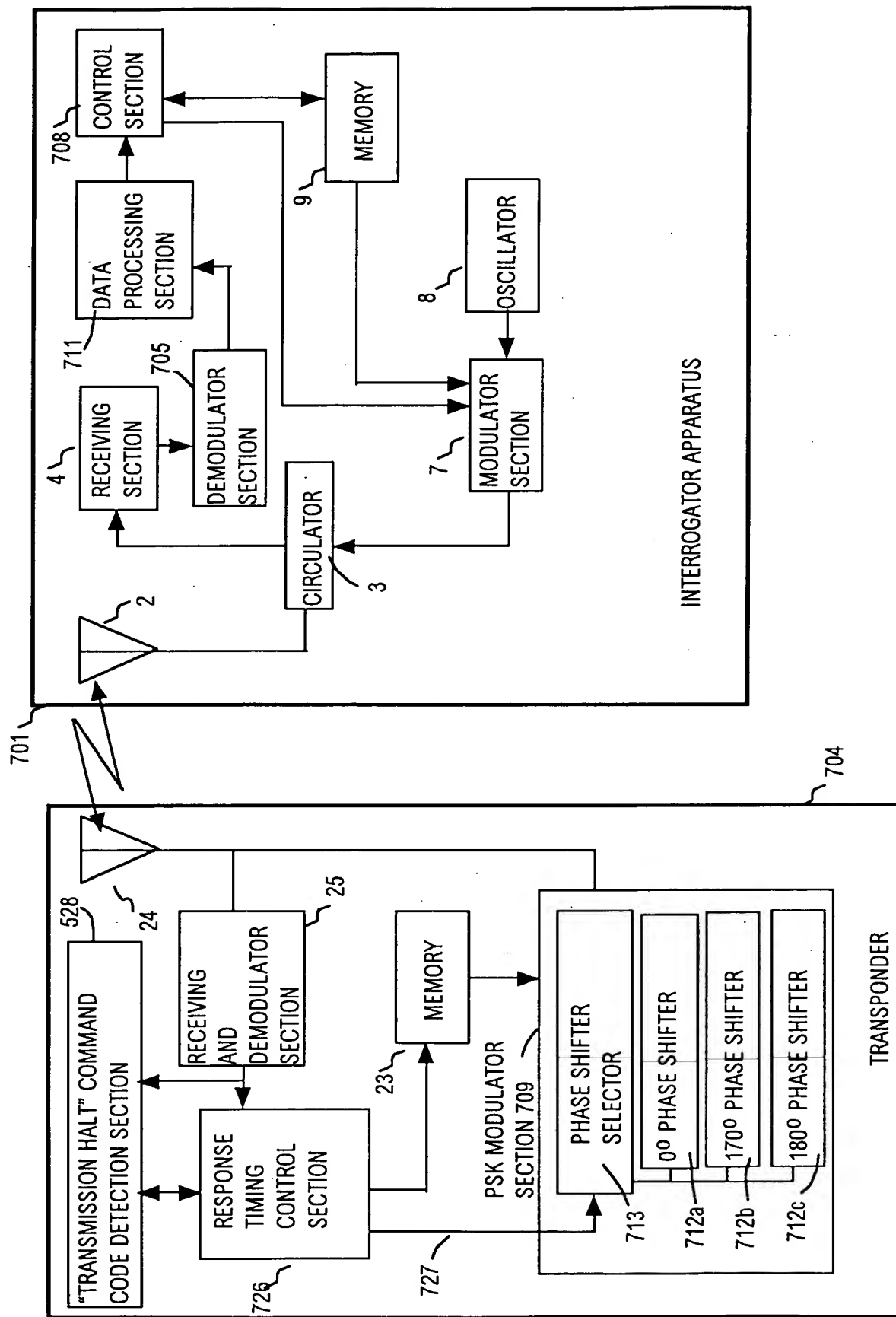
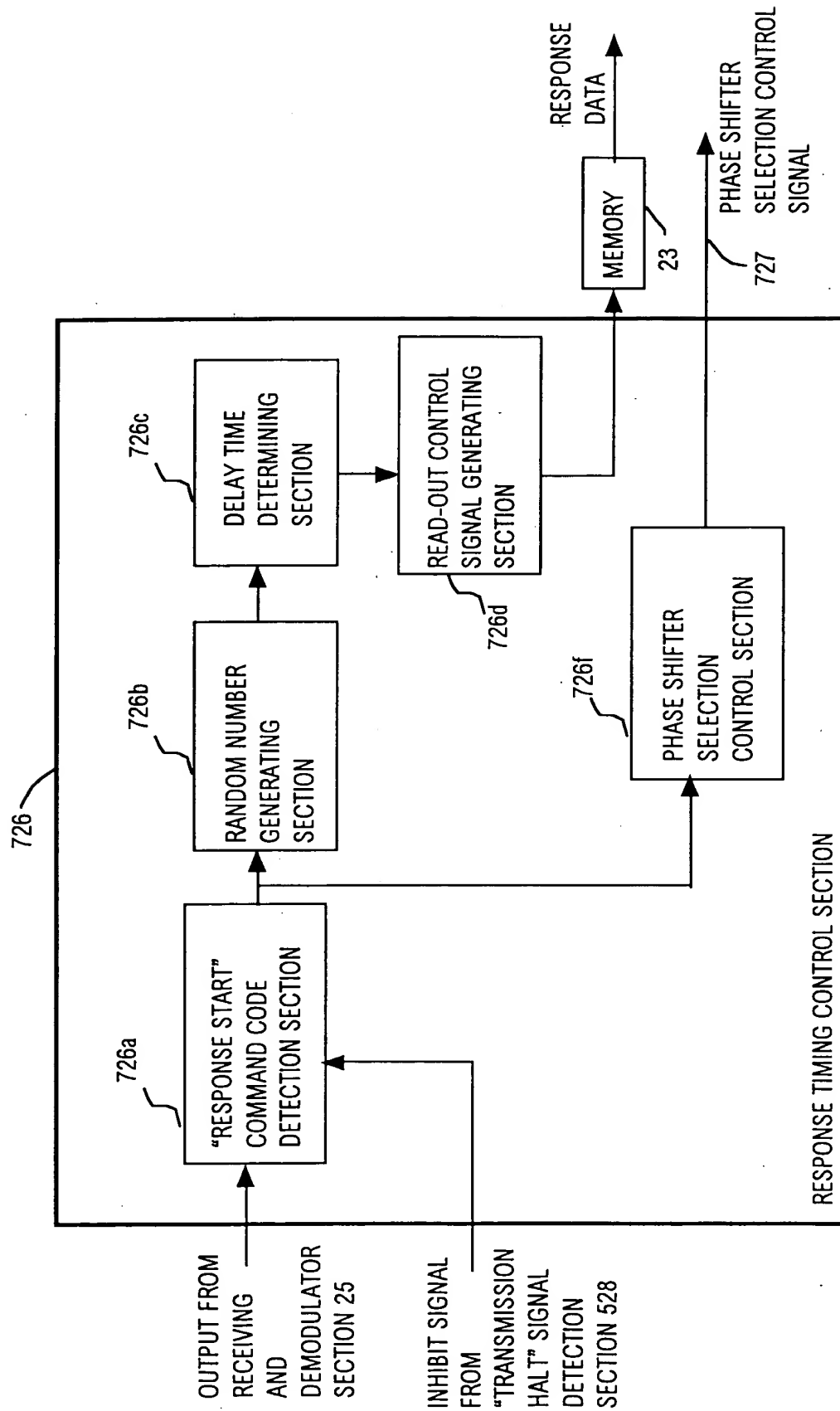


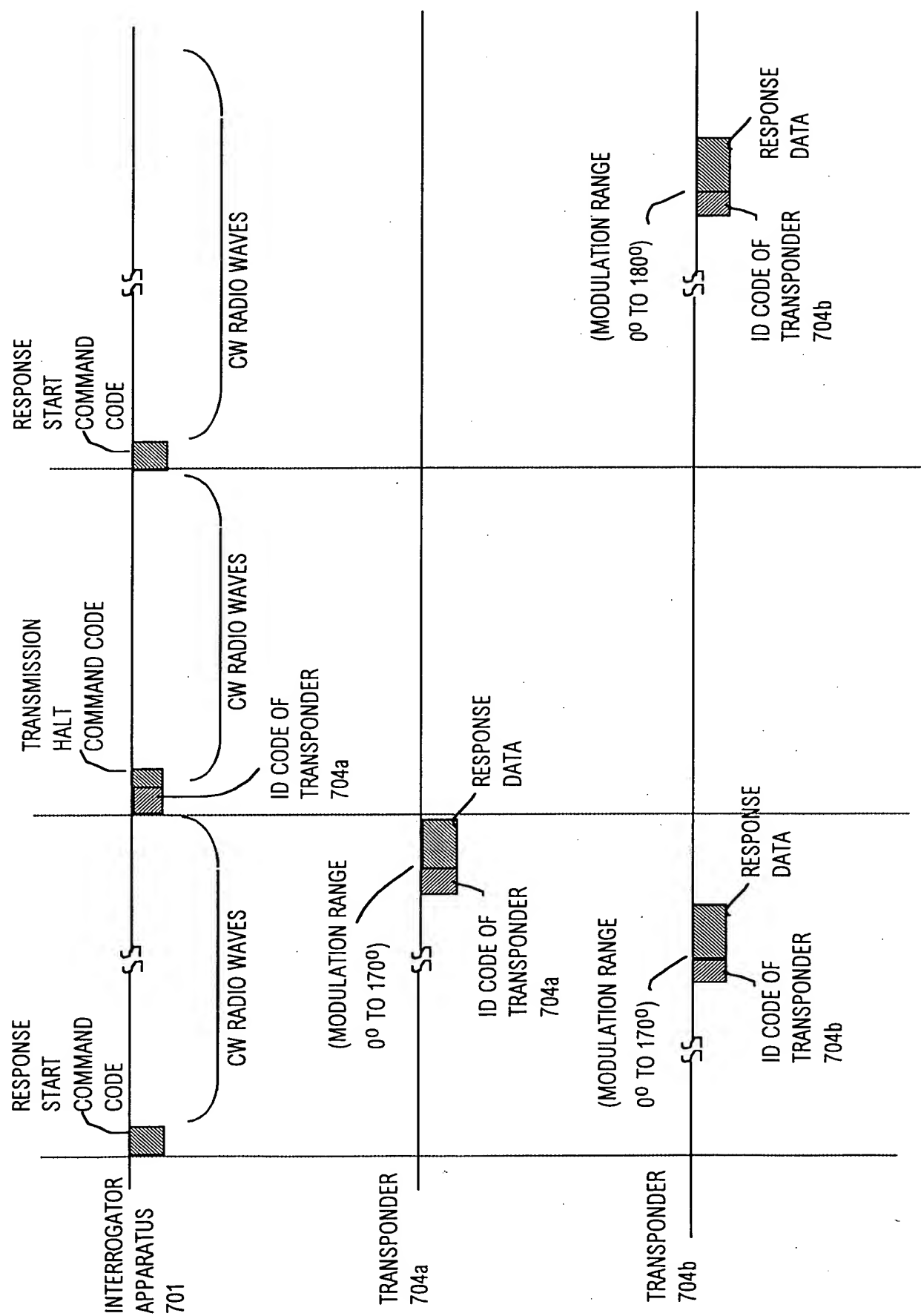
FIG. 45



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FIG. 46



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FIG. 47

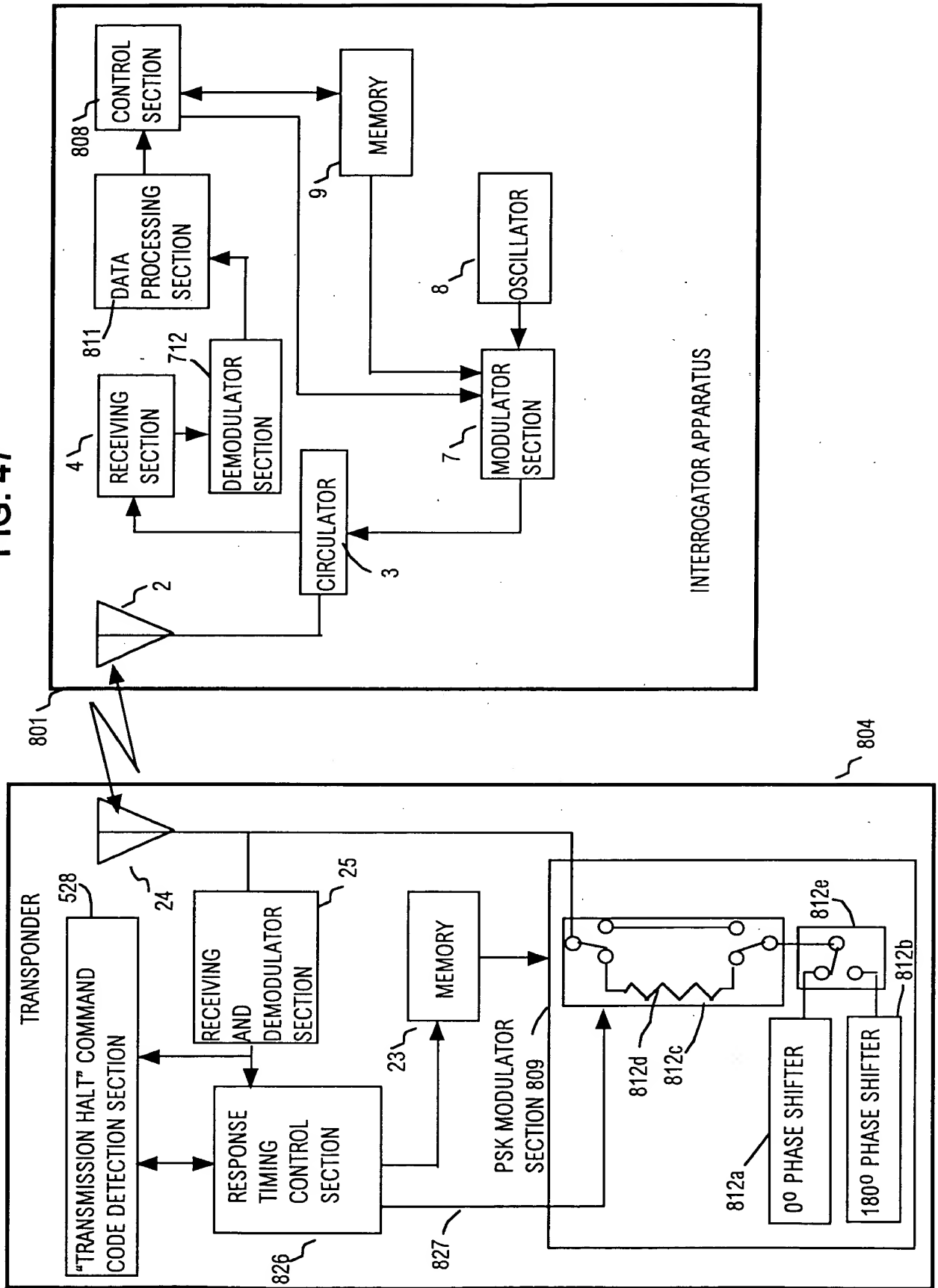


FIG. 48

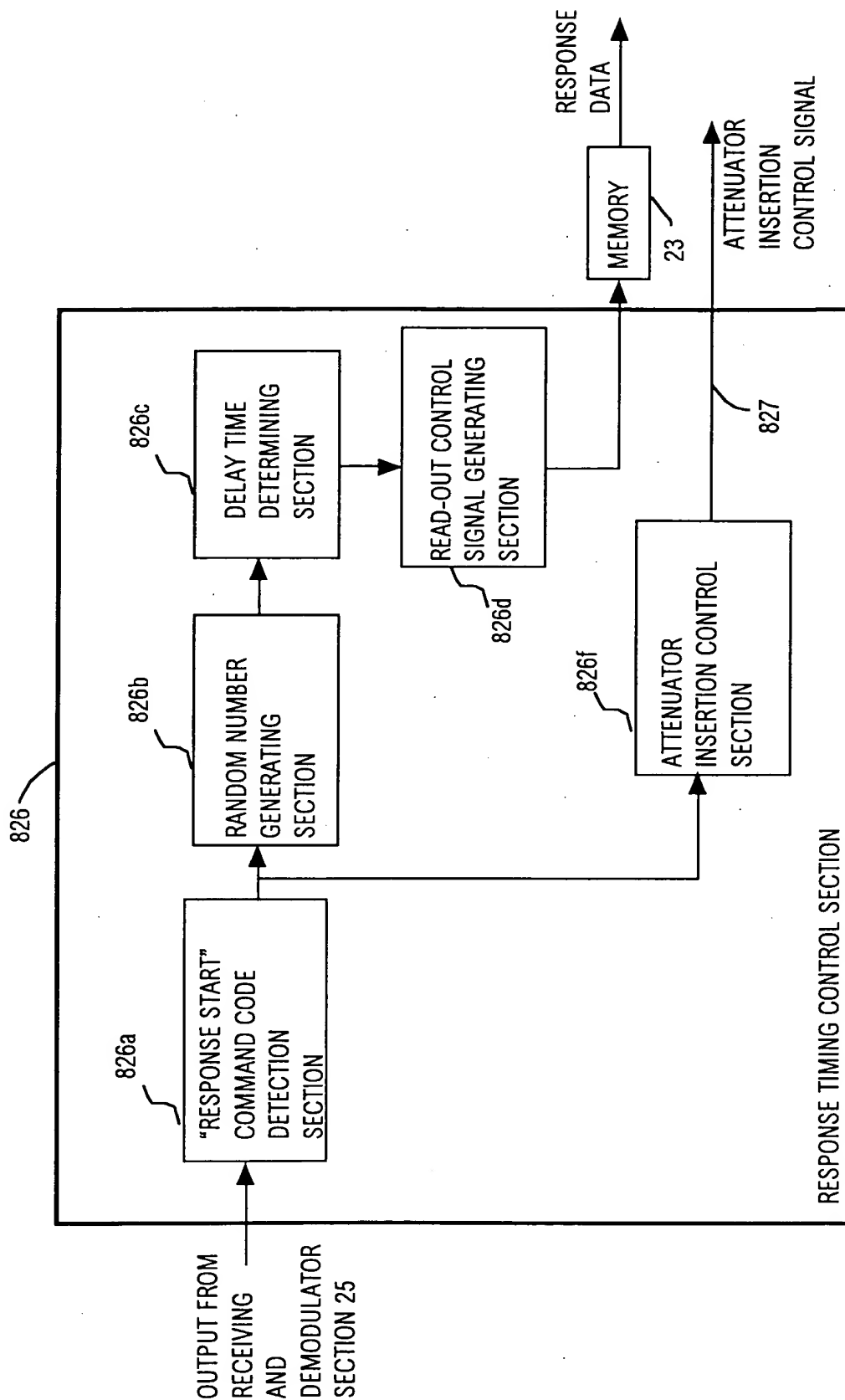


FIG. 49

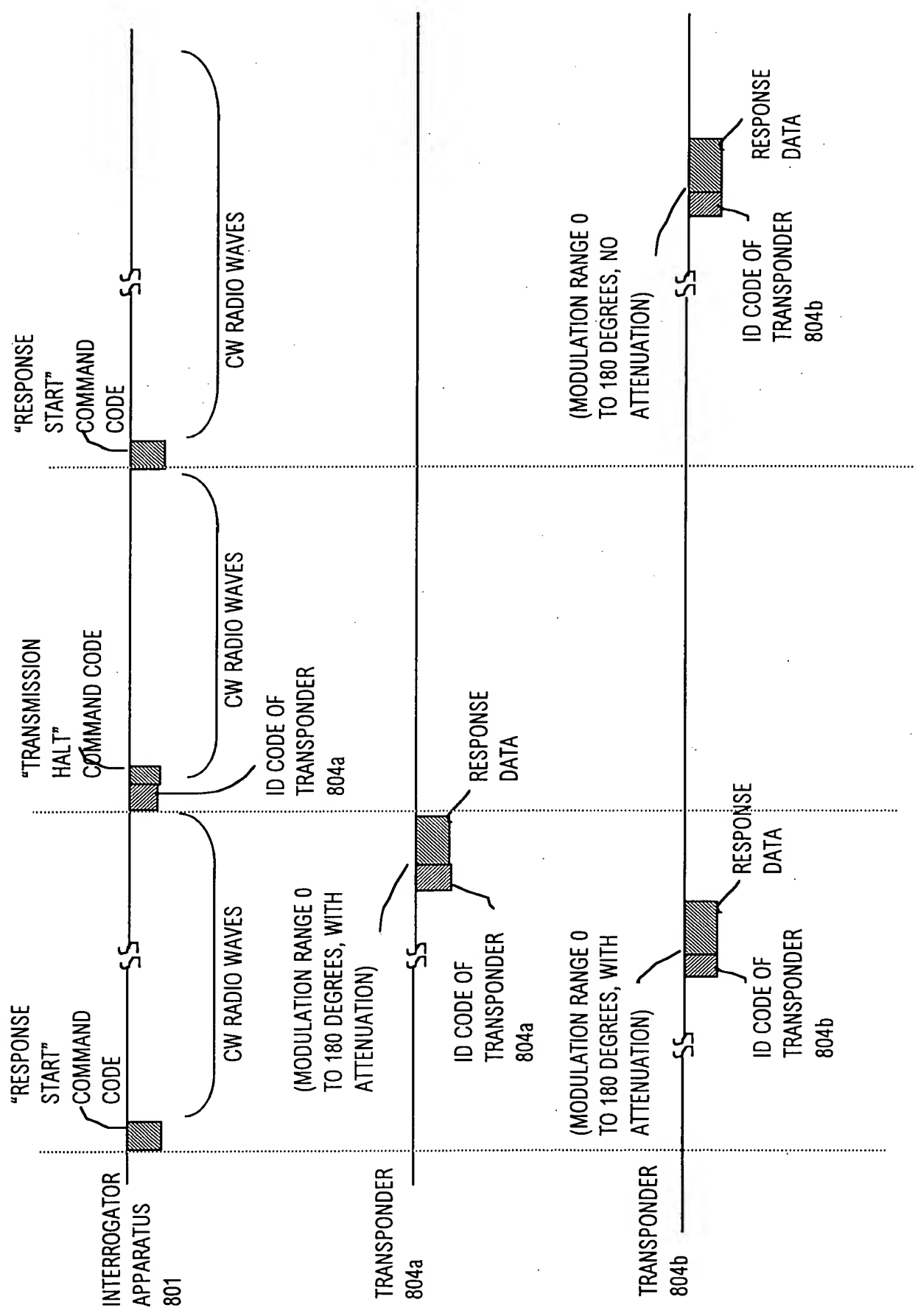


FIG. 50

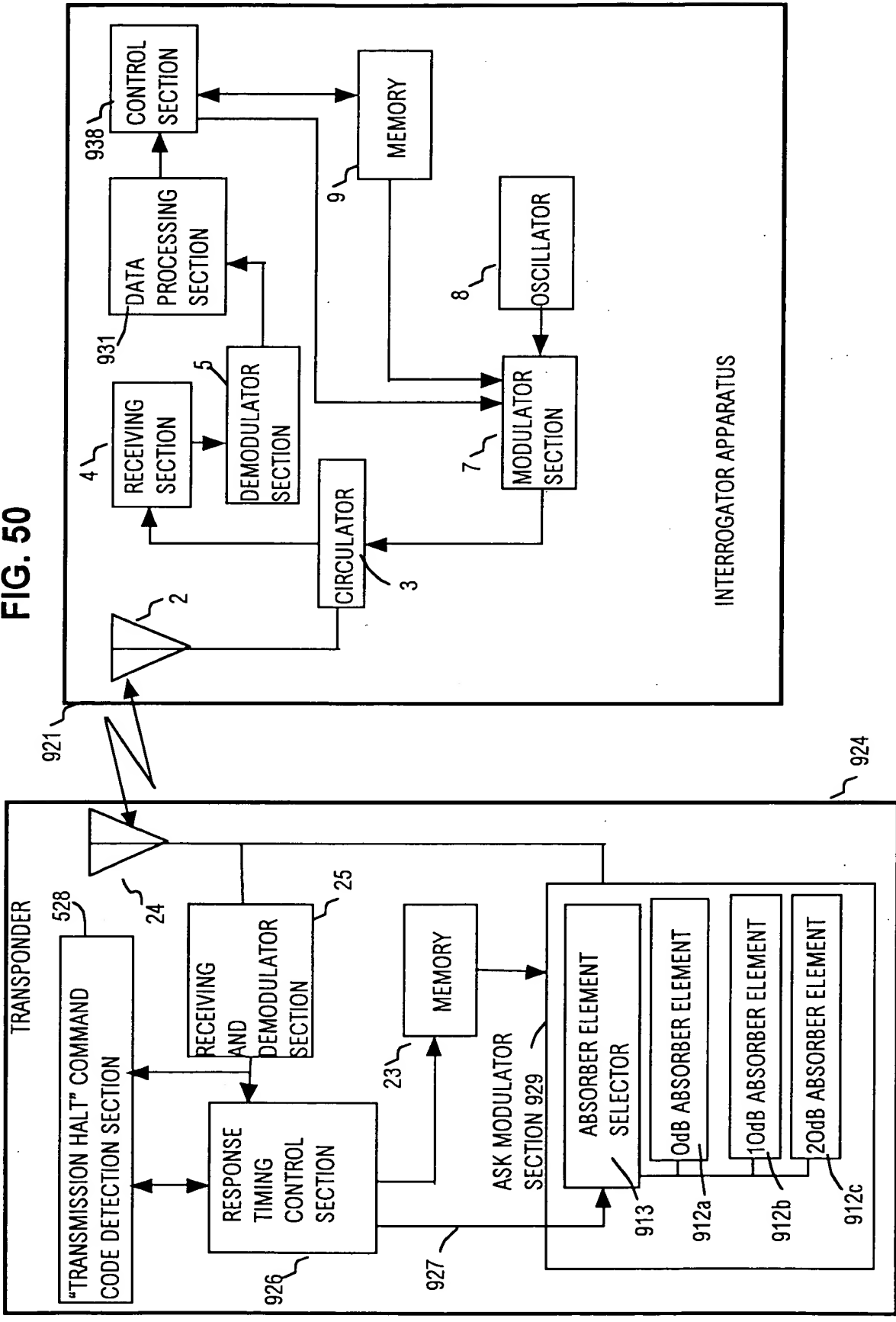


FIG 51

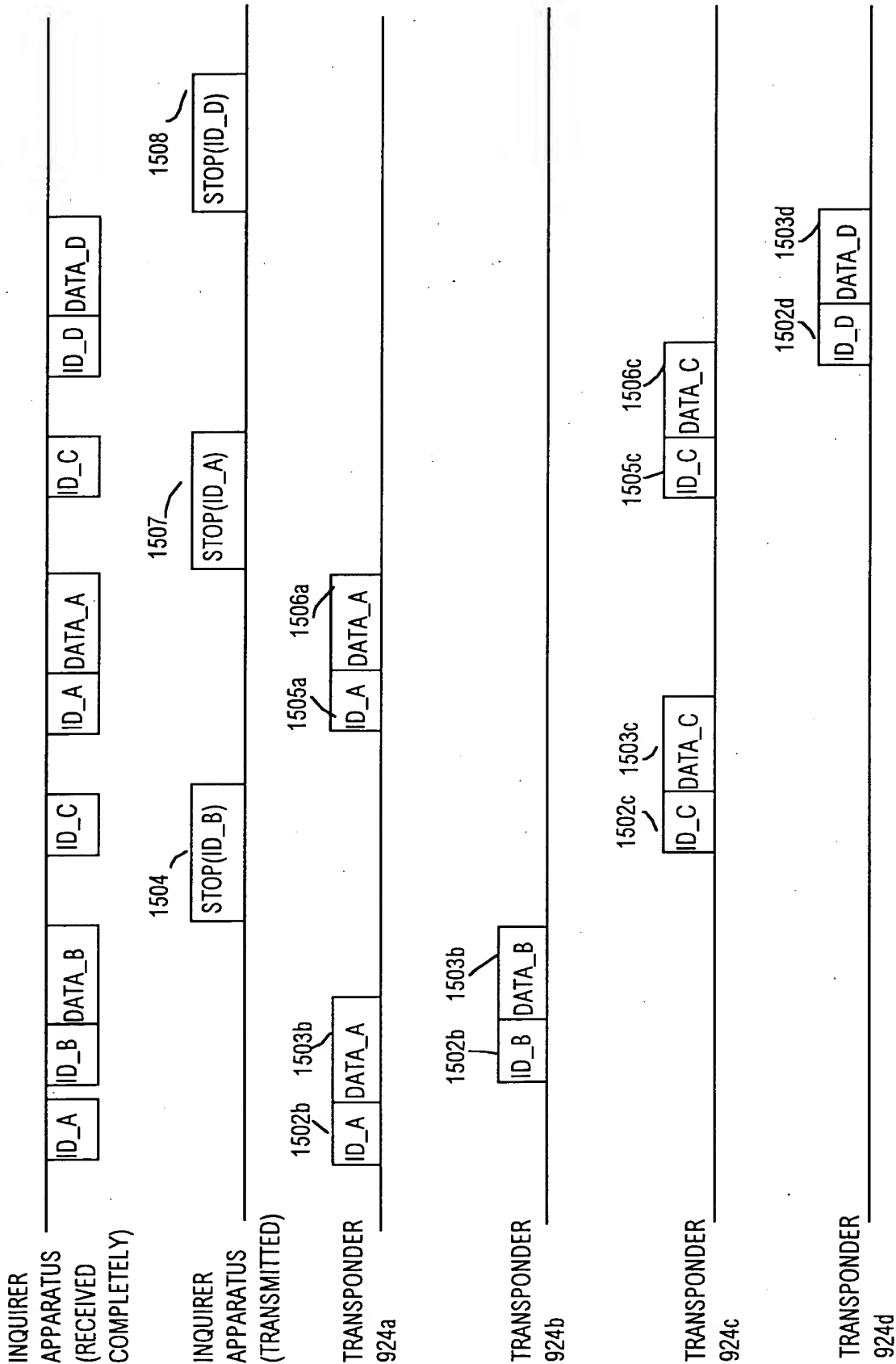




FIG. 52

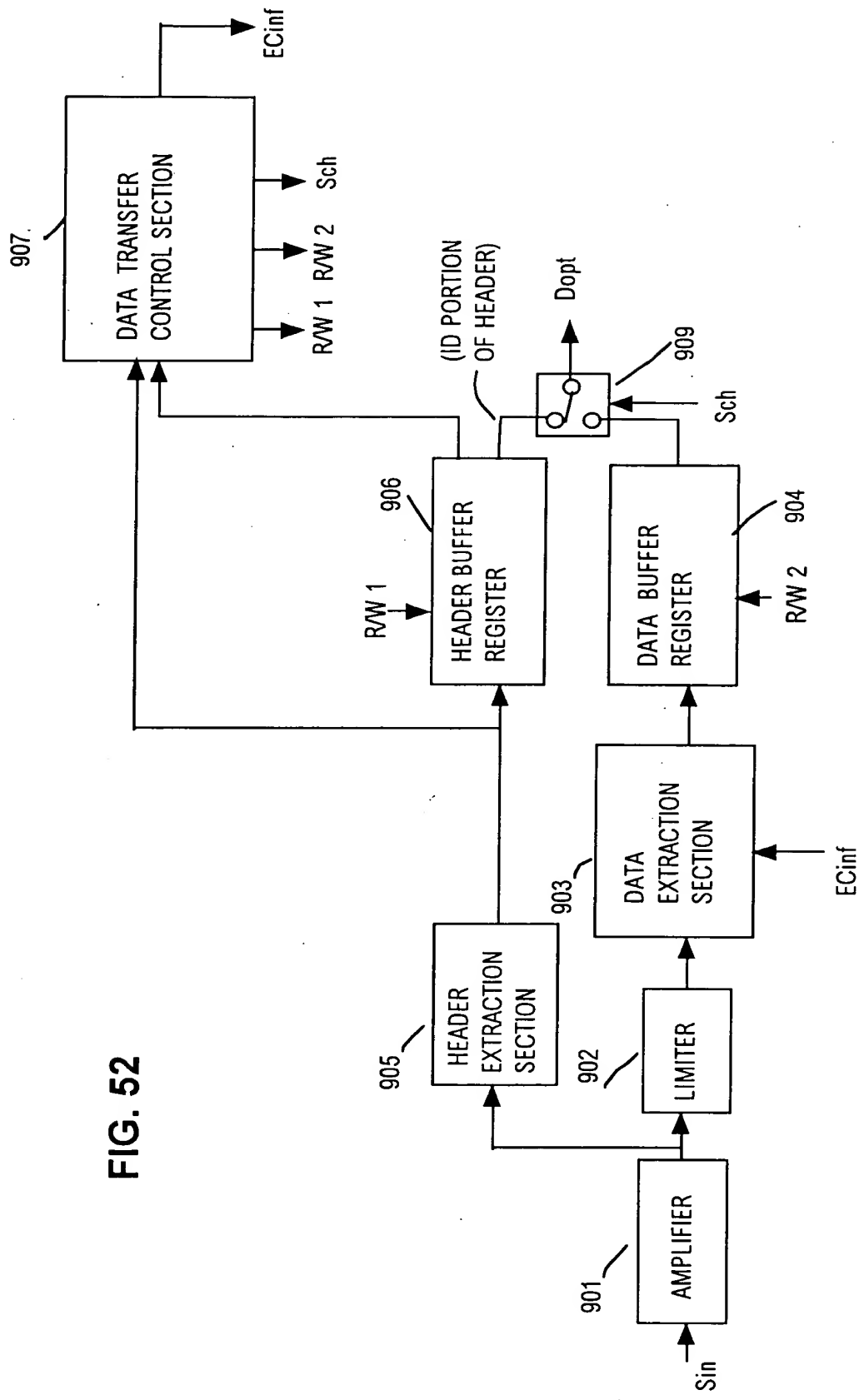


FIG. 53

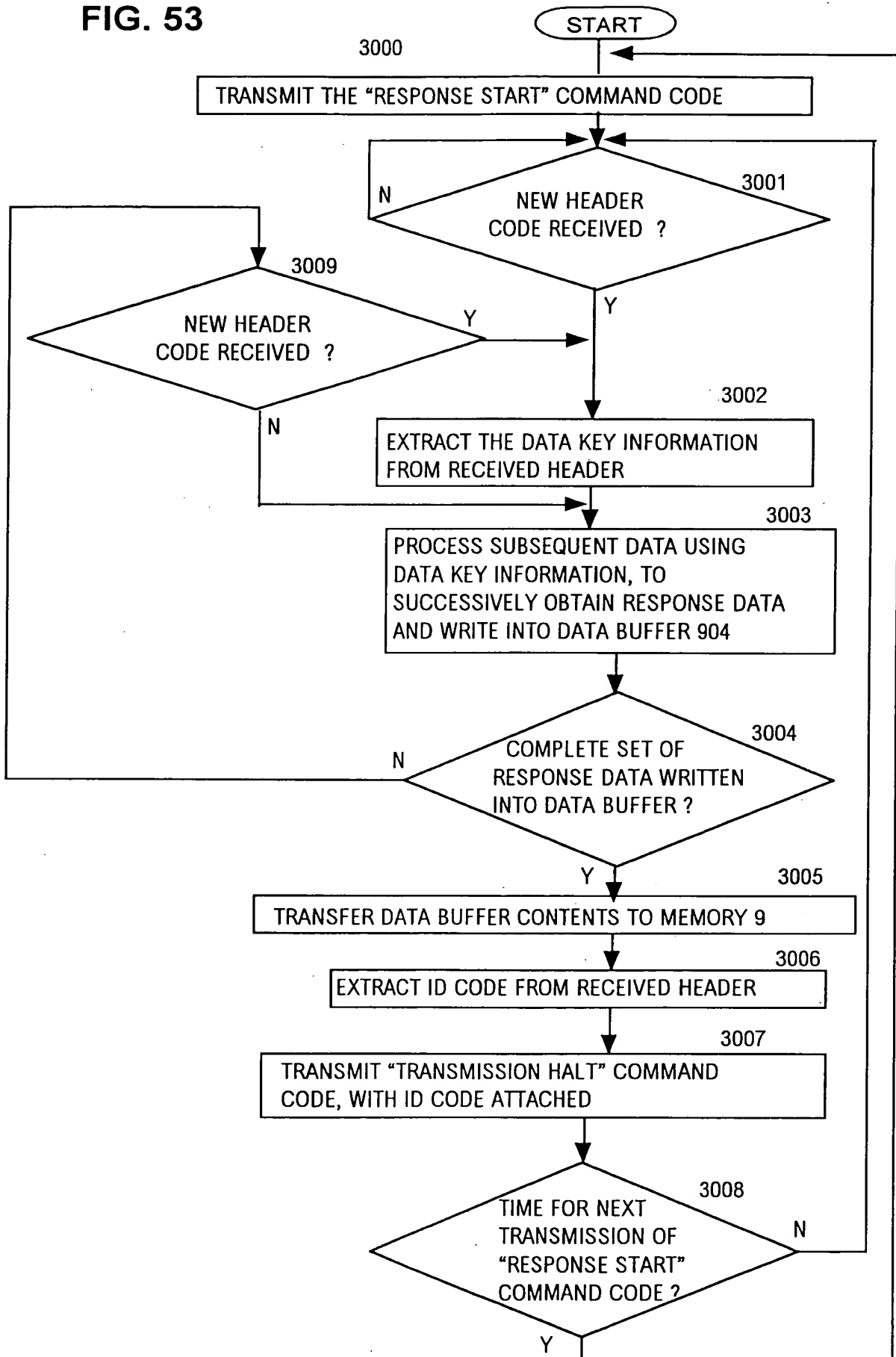


FIG. 54

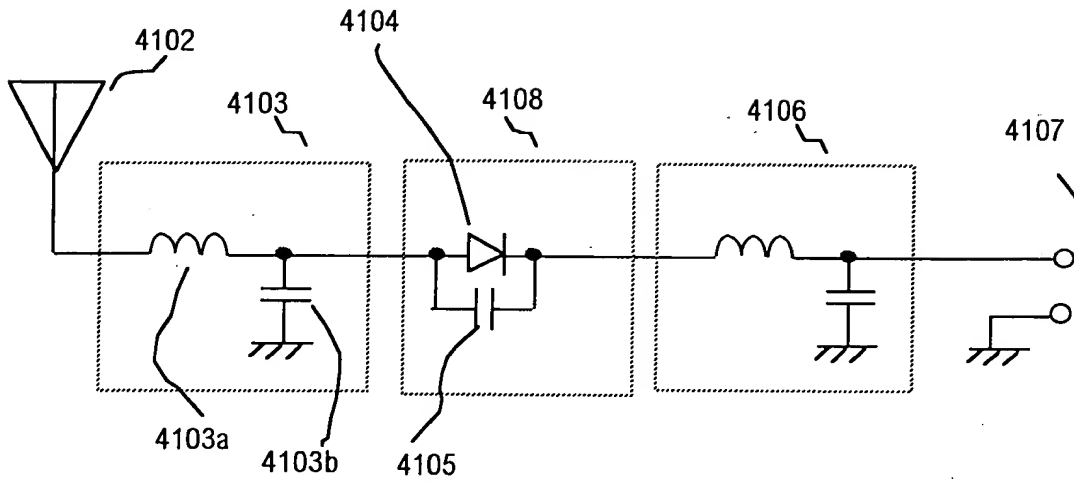
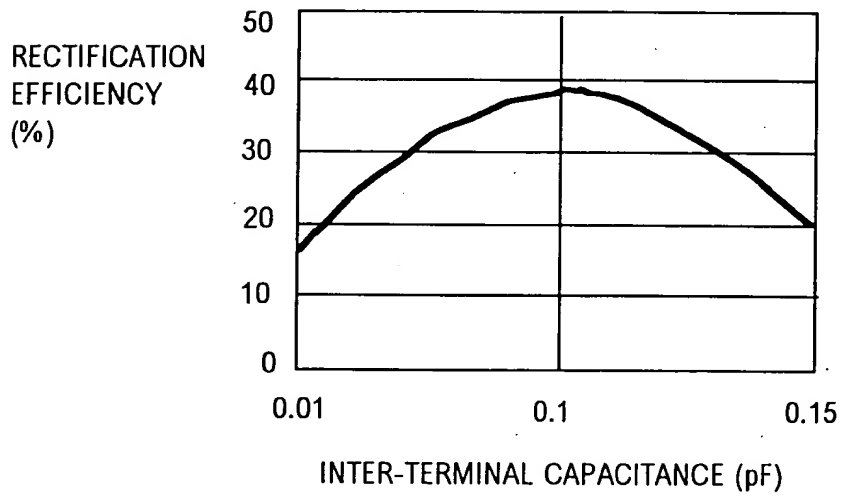


FIG. 55



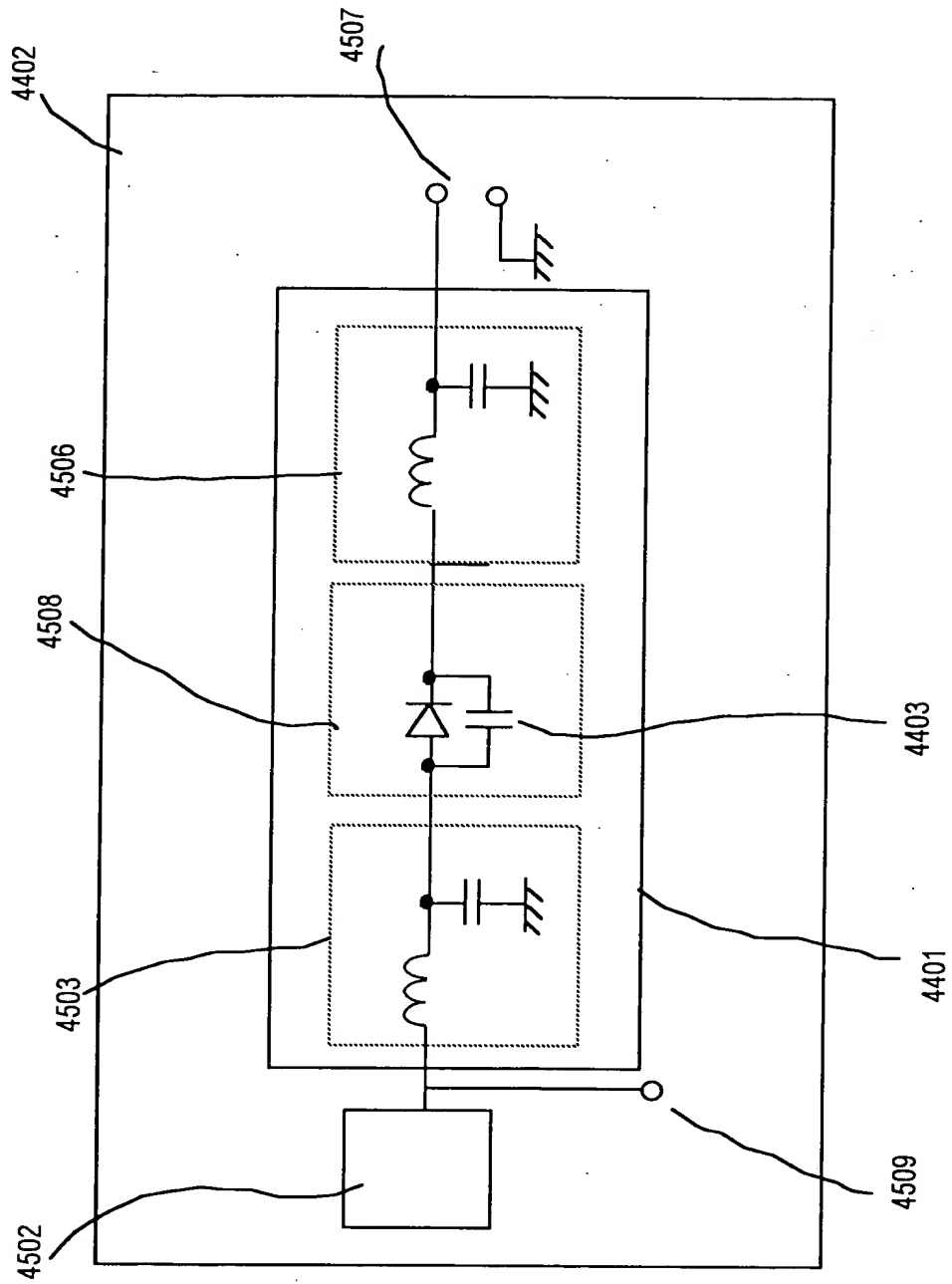
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FIG. 56



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FIG. 57

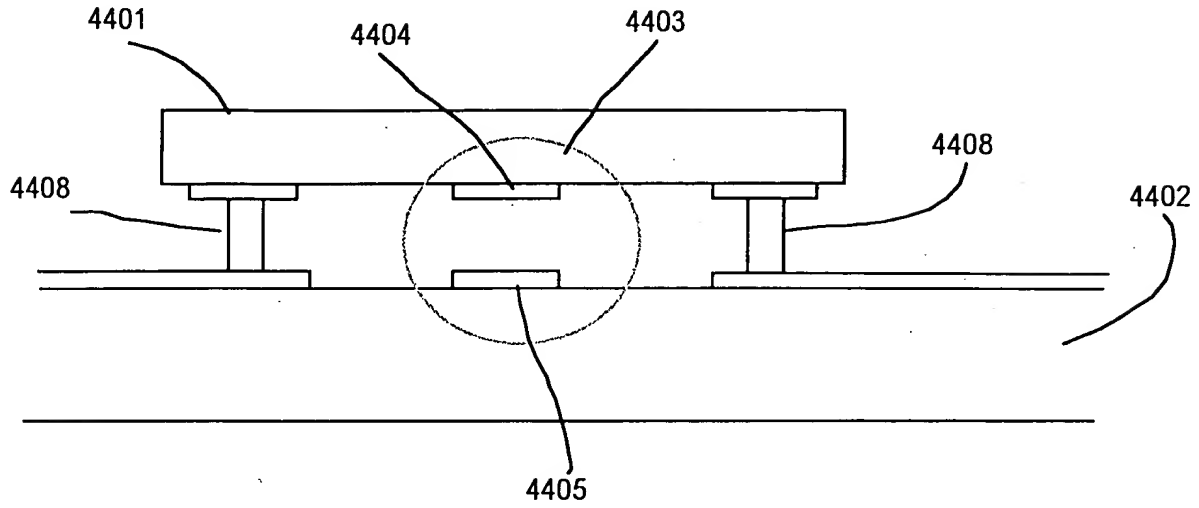
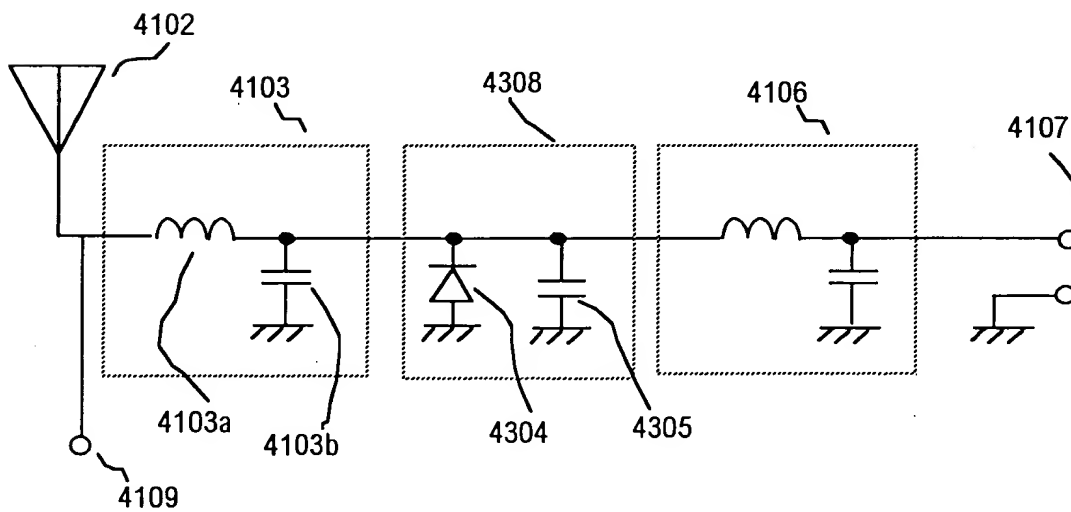
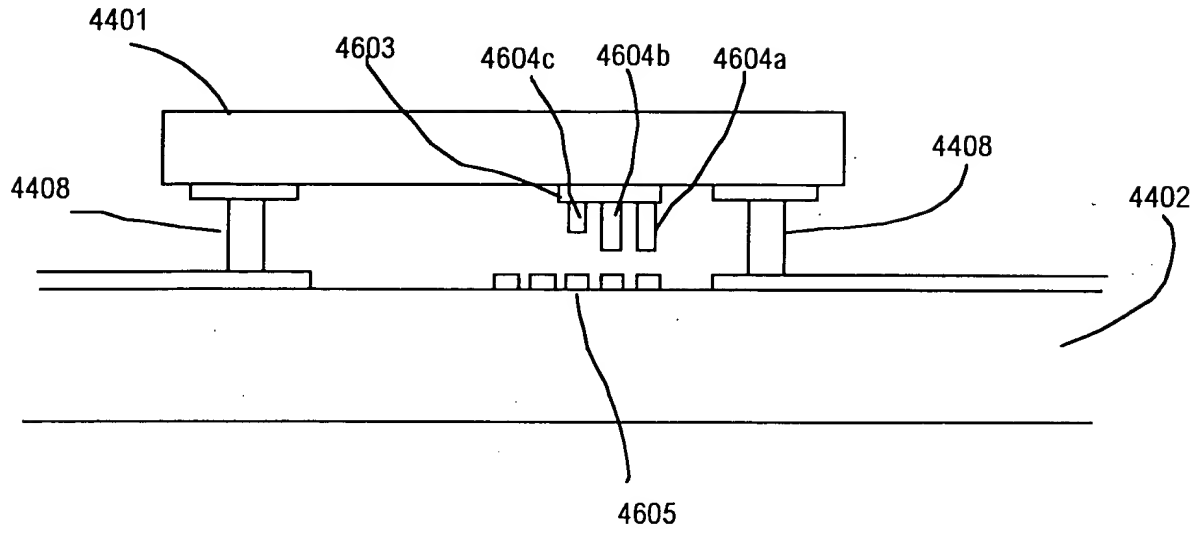


FIG. 58



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FIG. 59



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FIG. 60

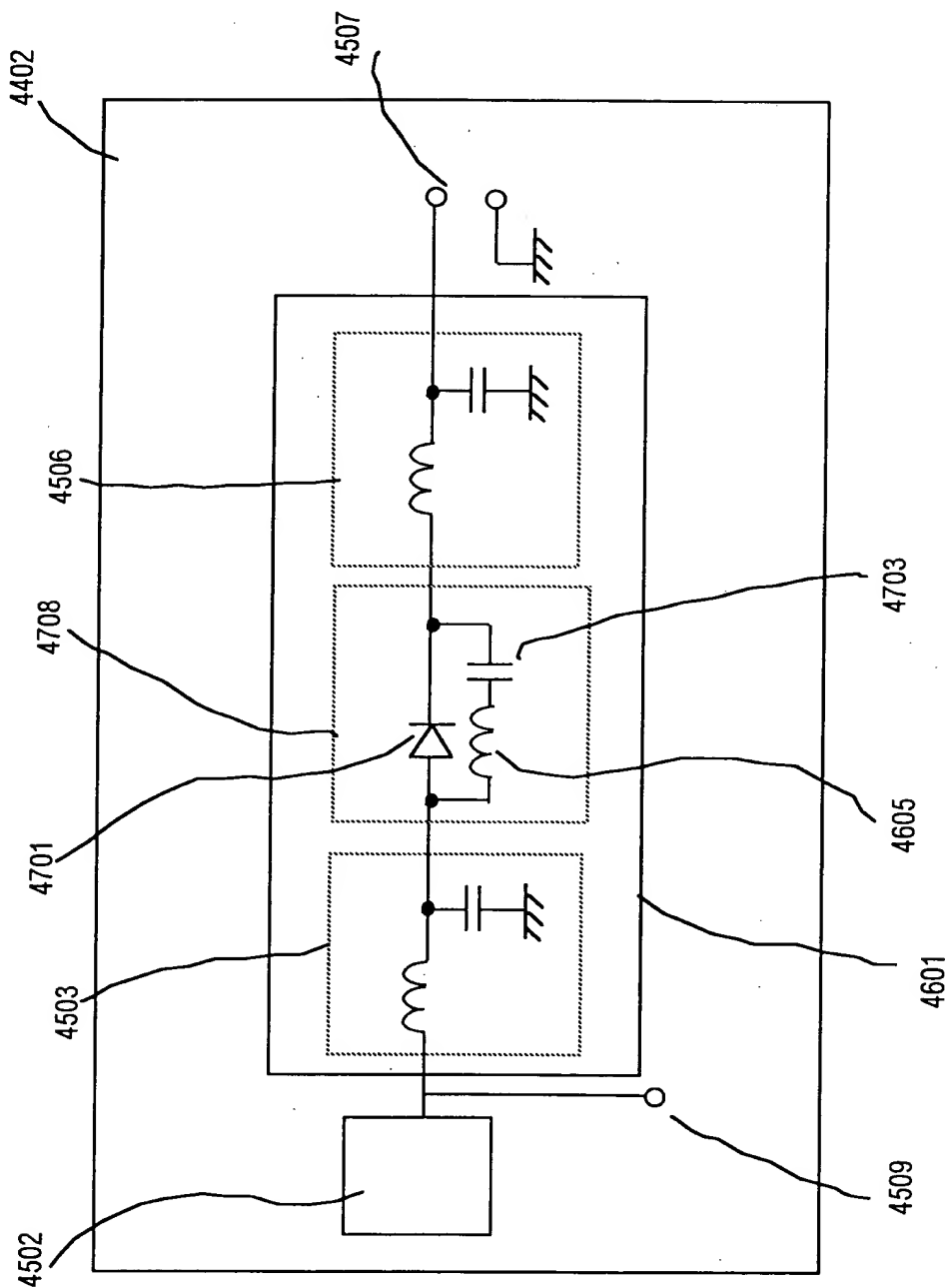
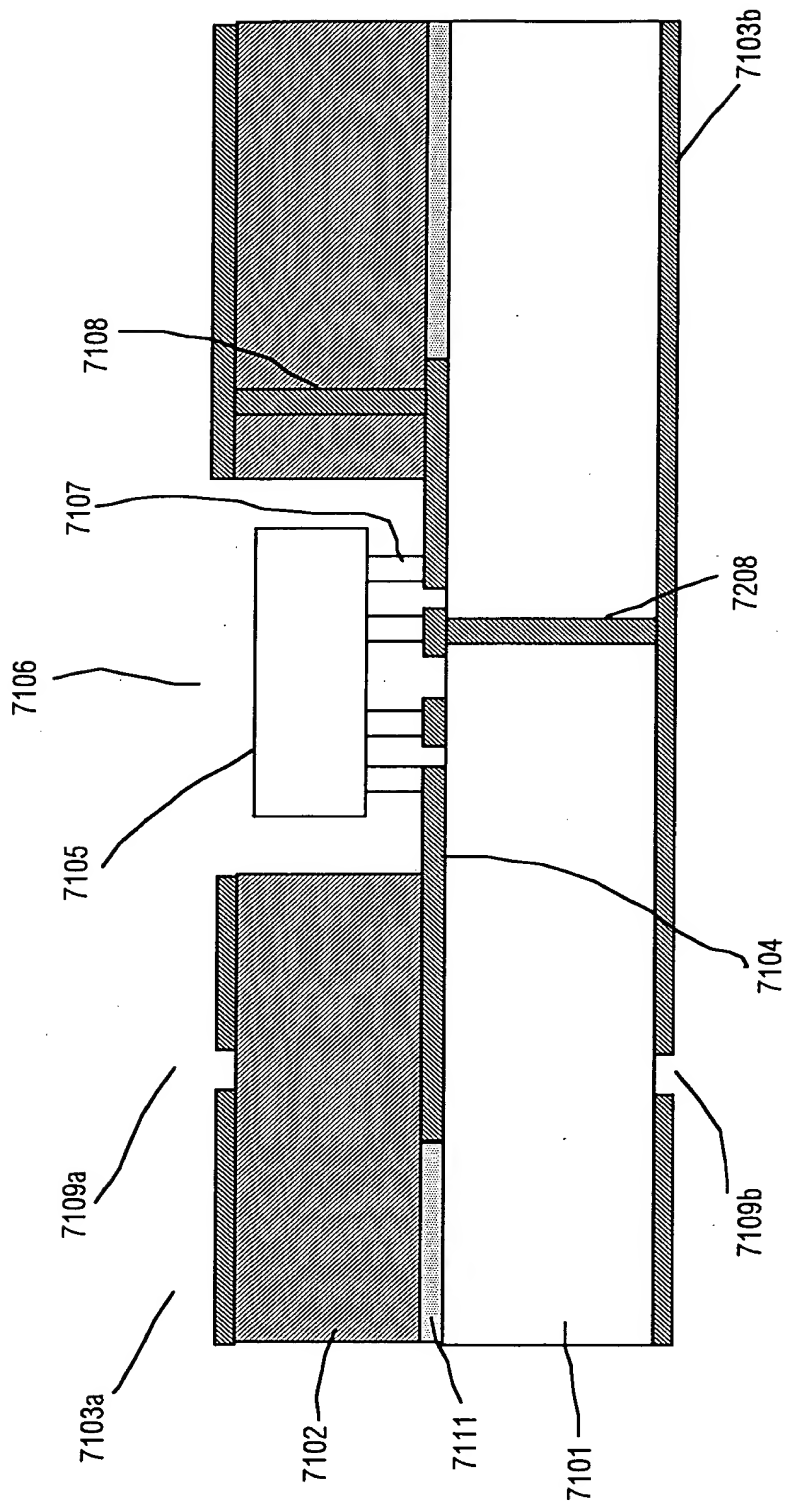


FIG. 61





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FIG. 62

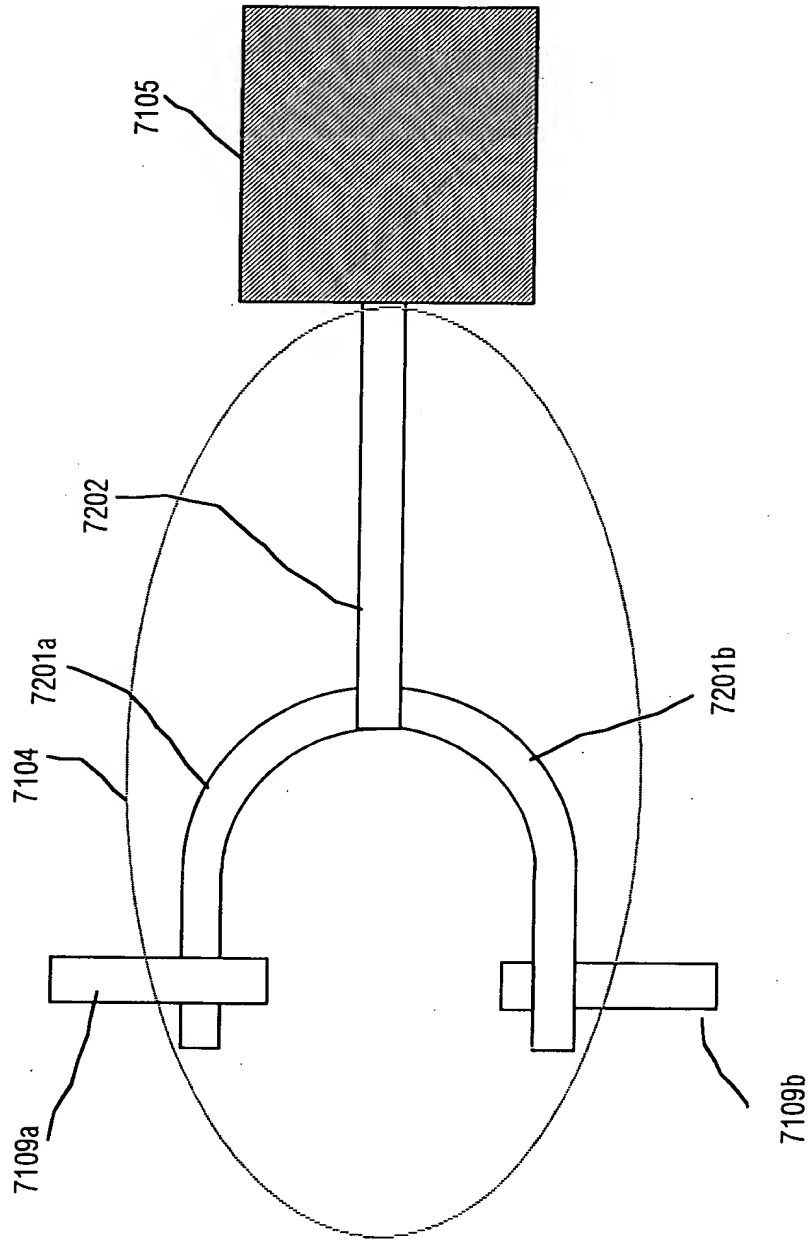
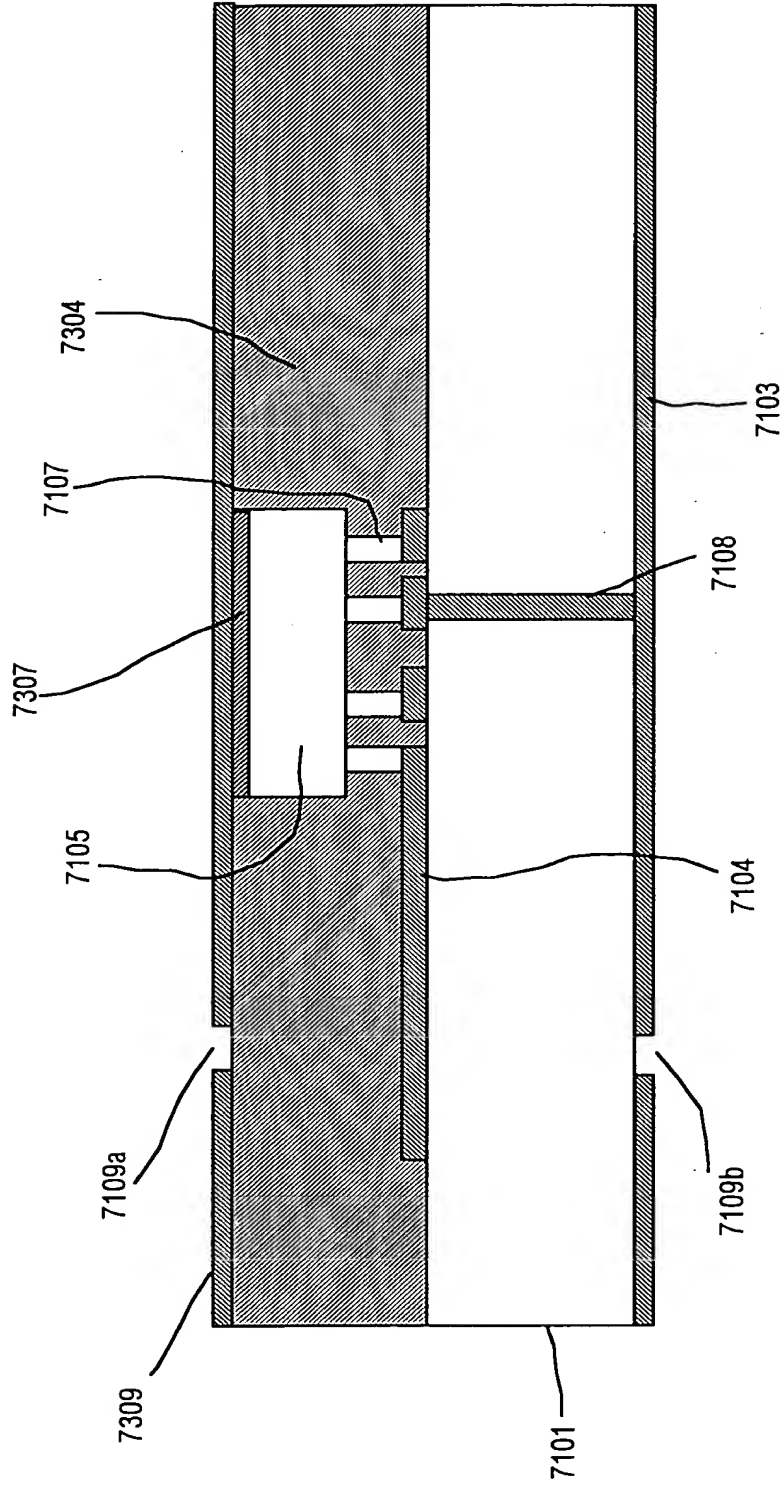


FIG. 63



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FIG. 64

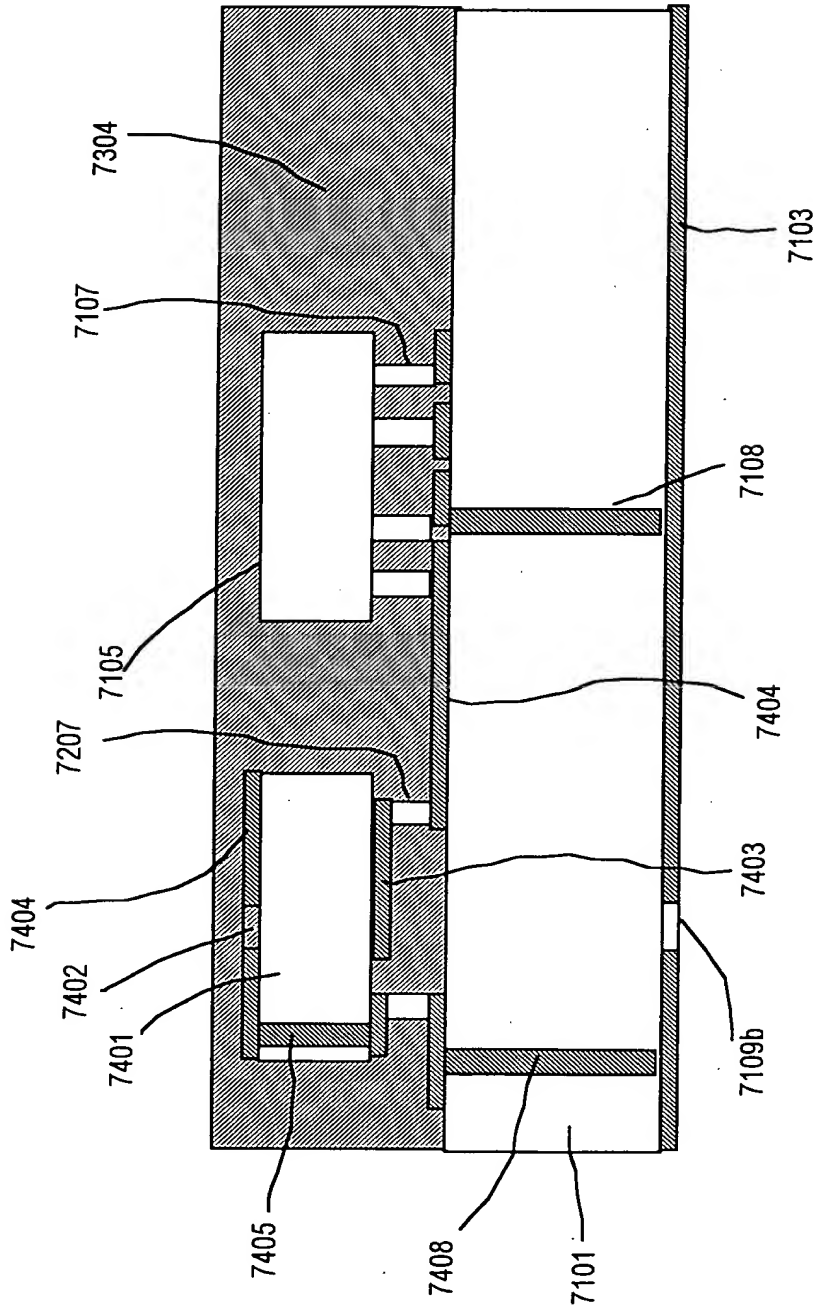
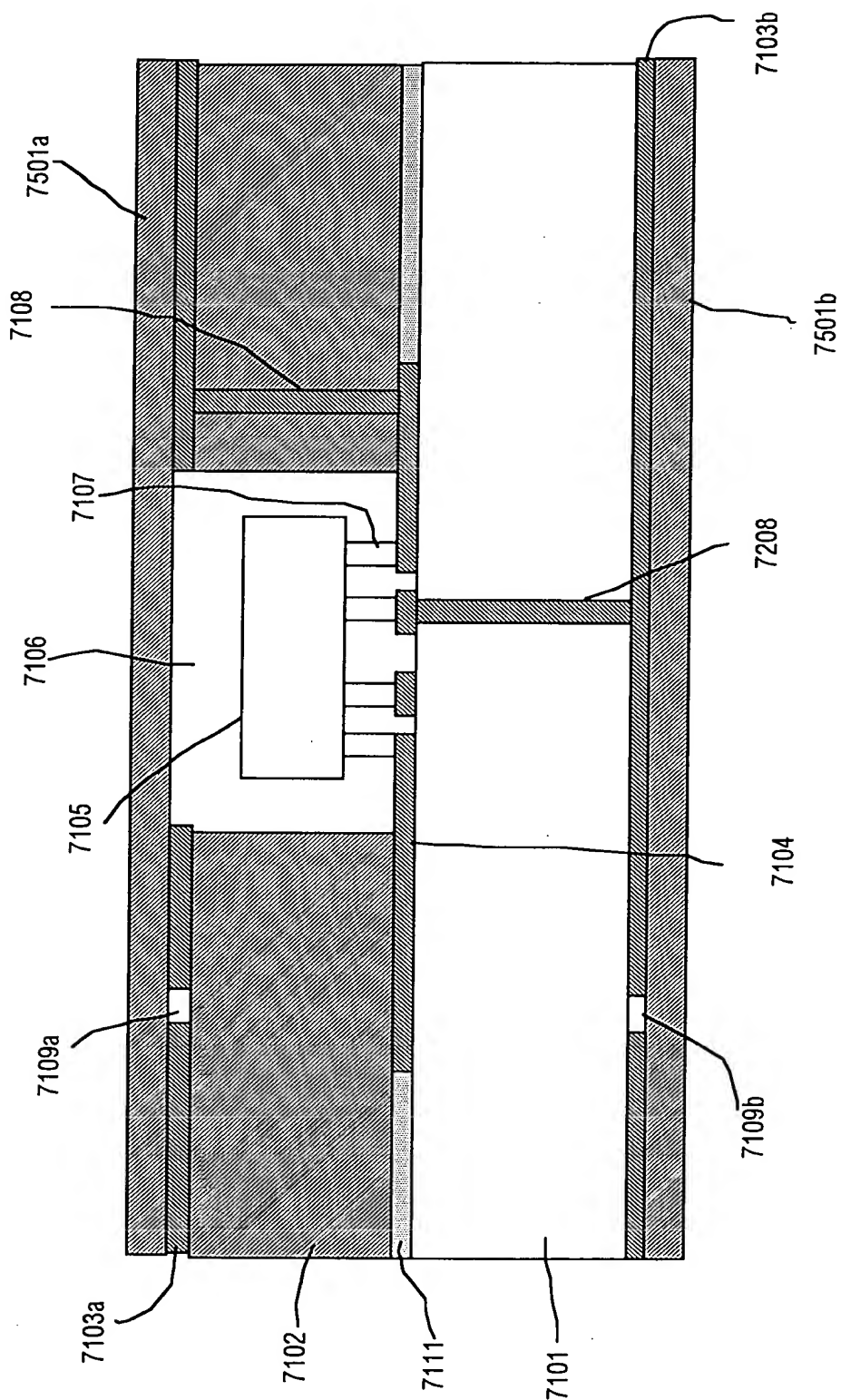


FIG. 65



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FIG. 66

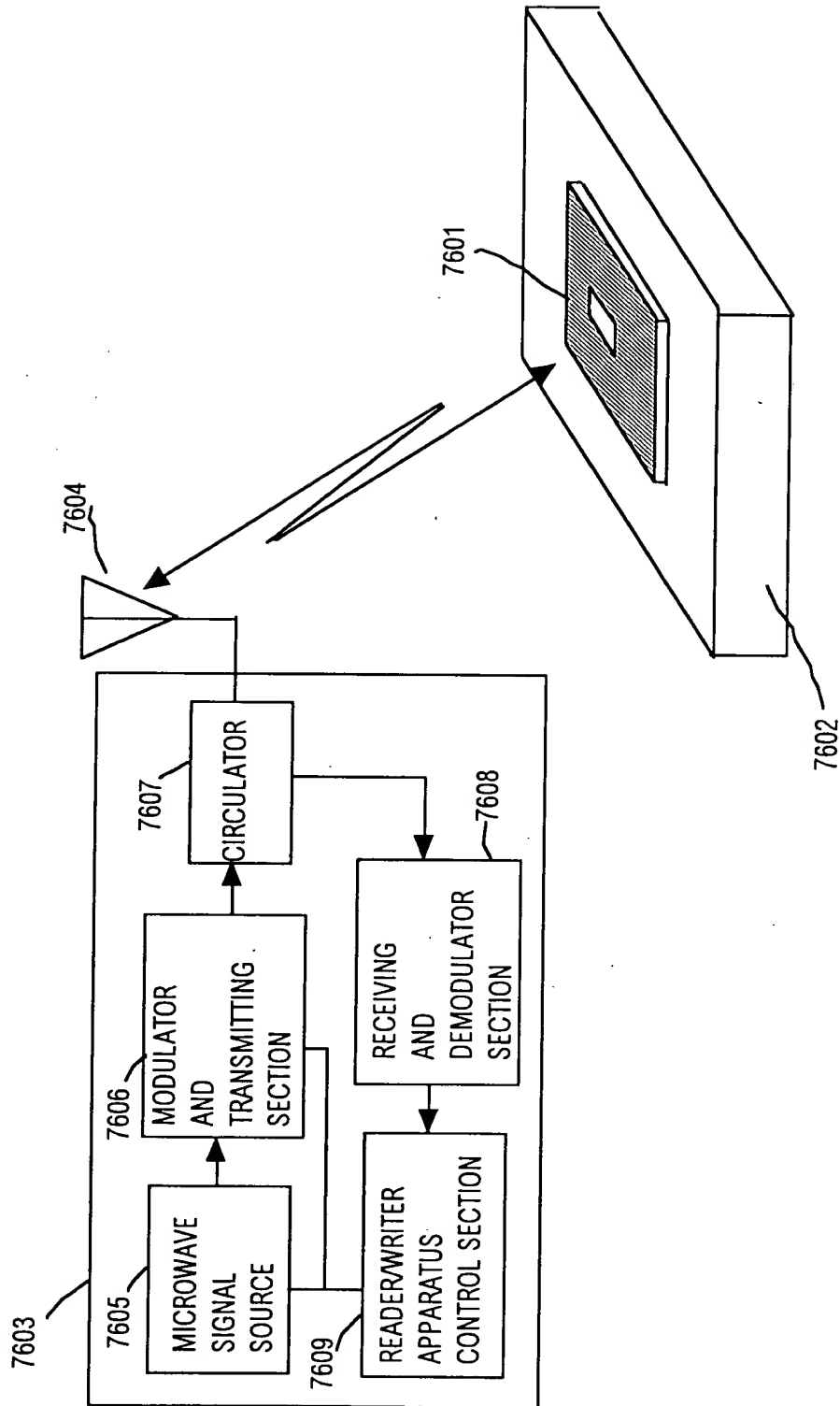


FIG. 67A

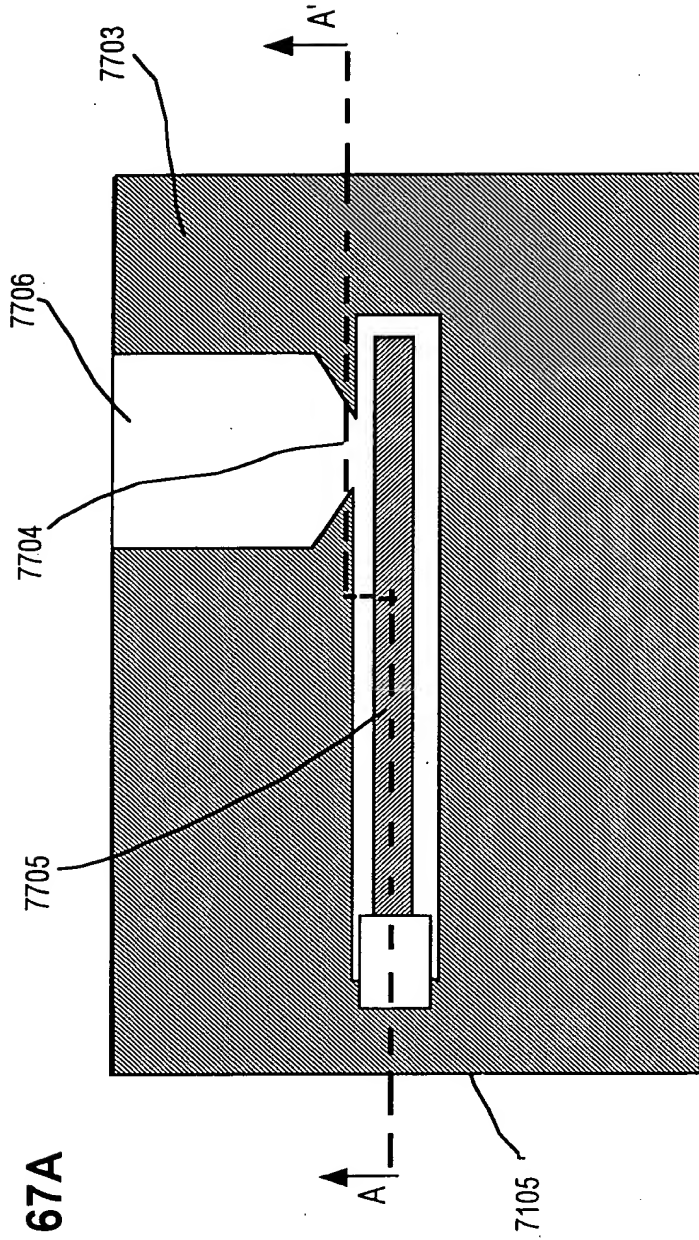
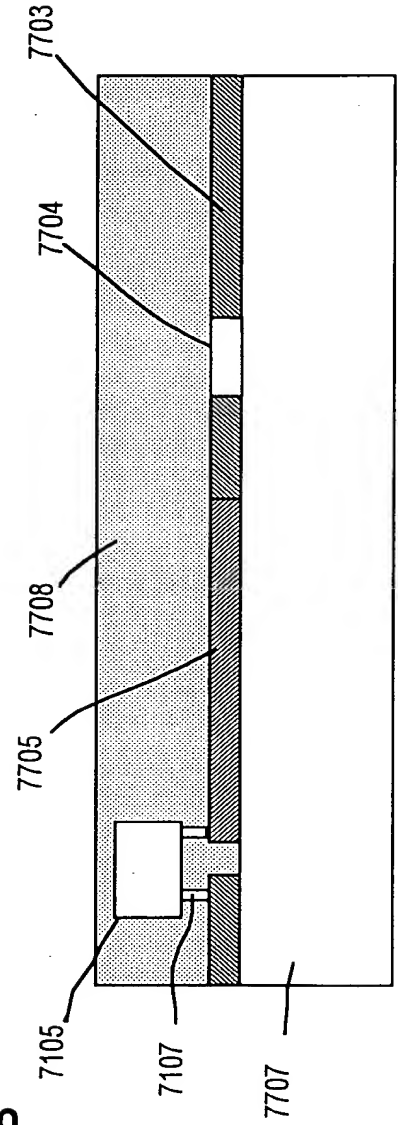


FIG. 67B



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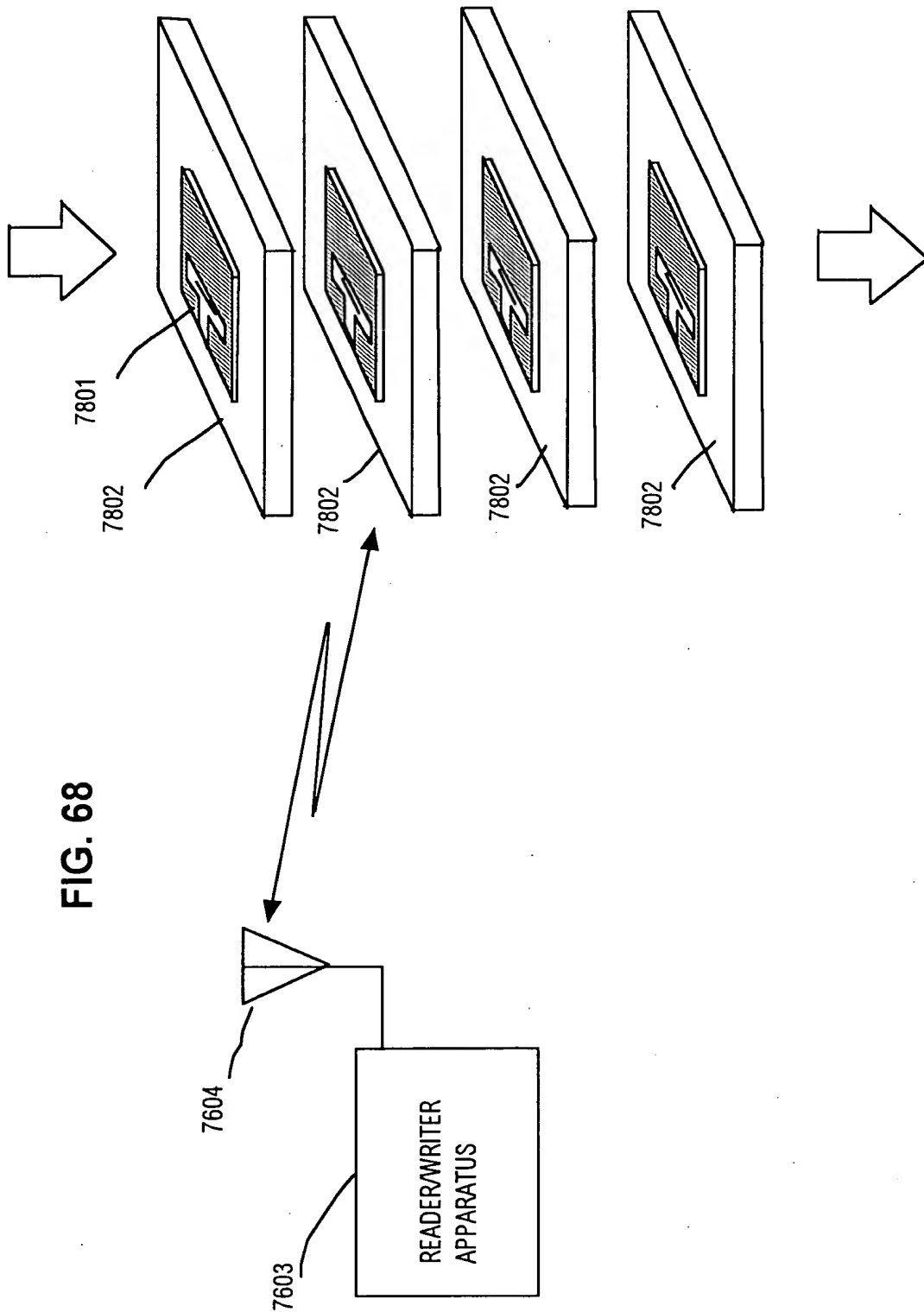


FIG. 69

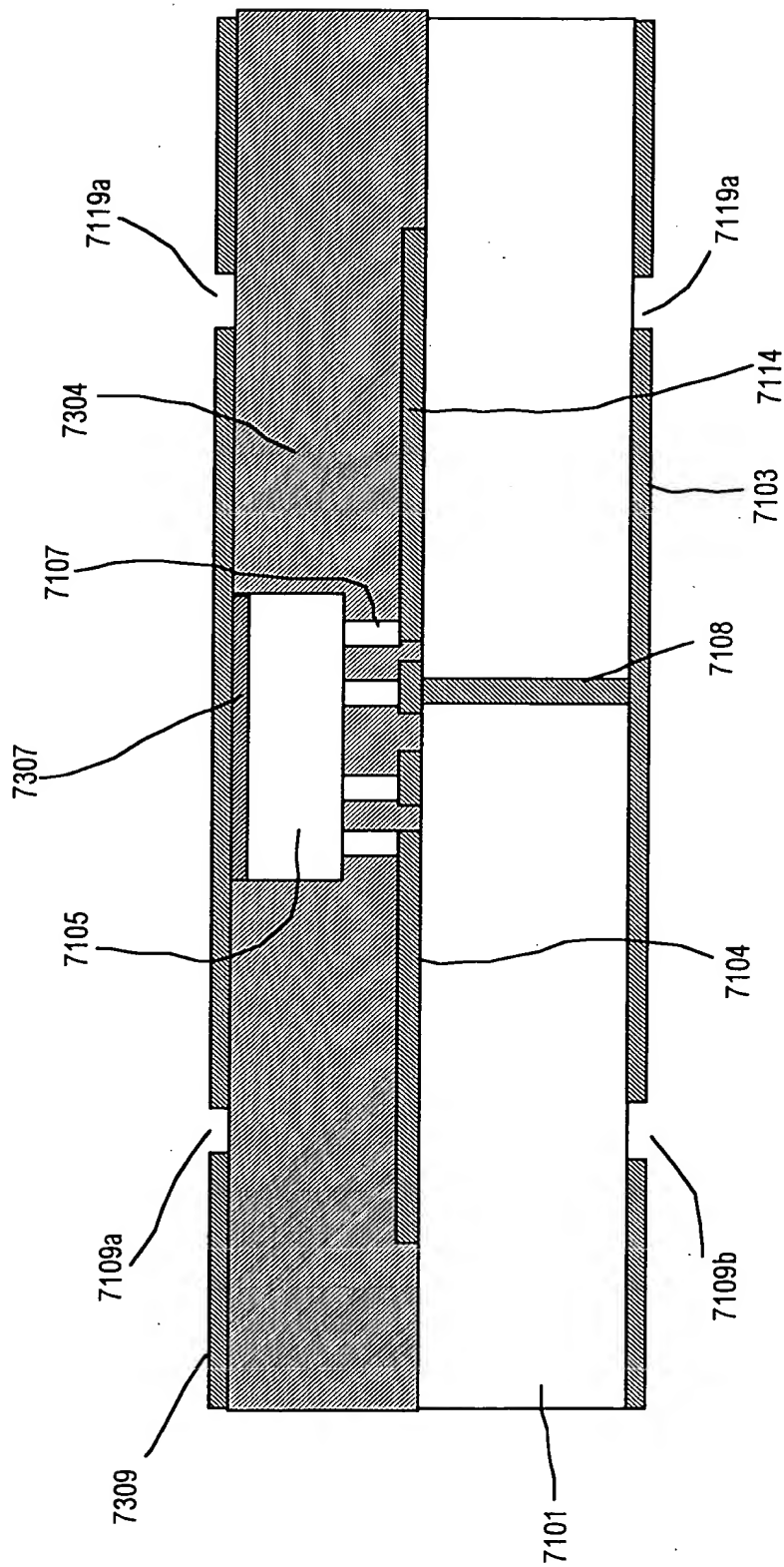




FIG. 70

PRIOR ART

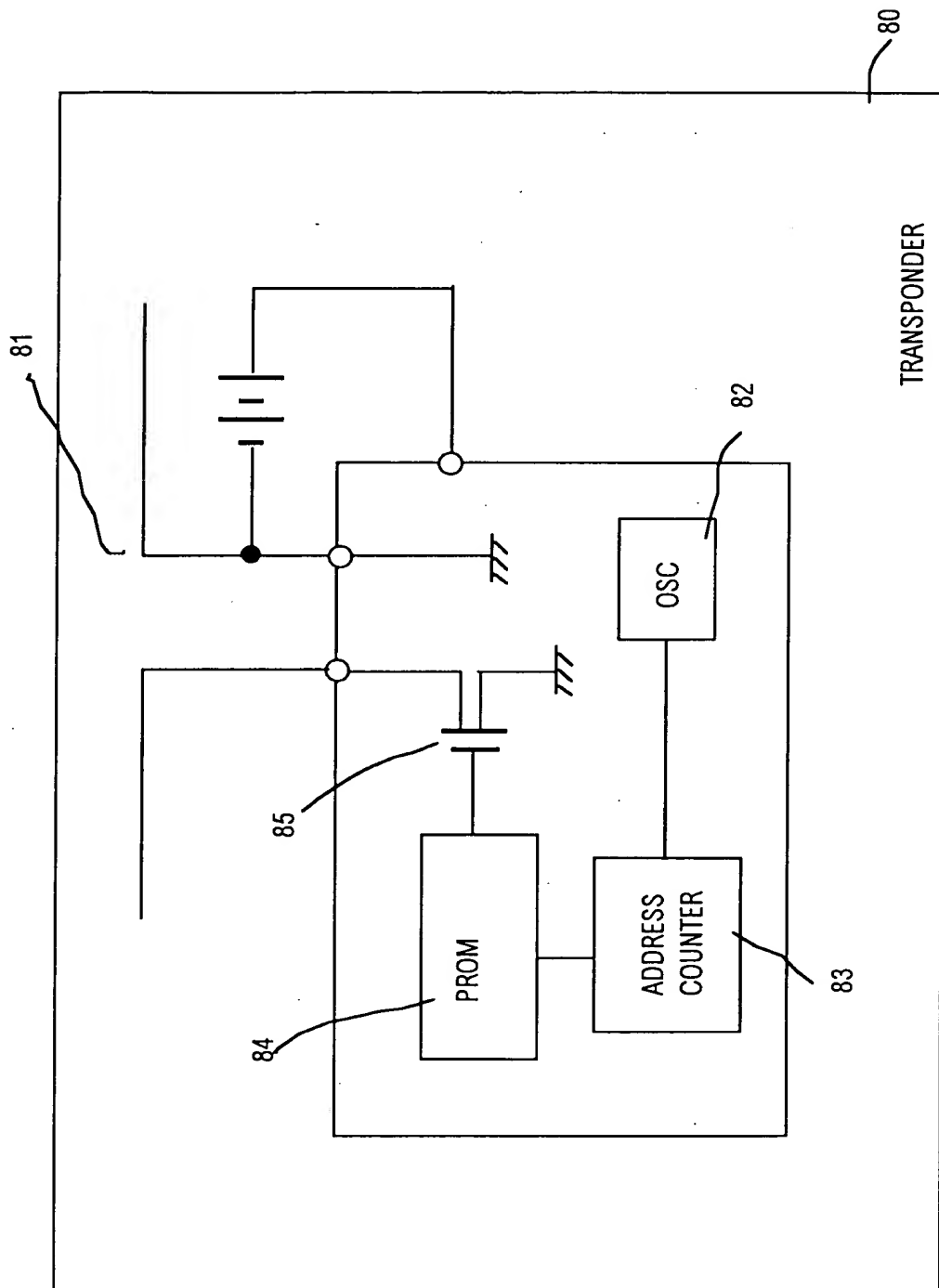
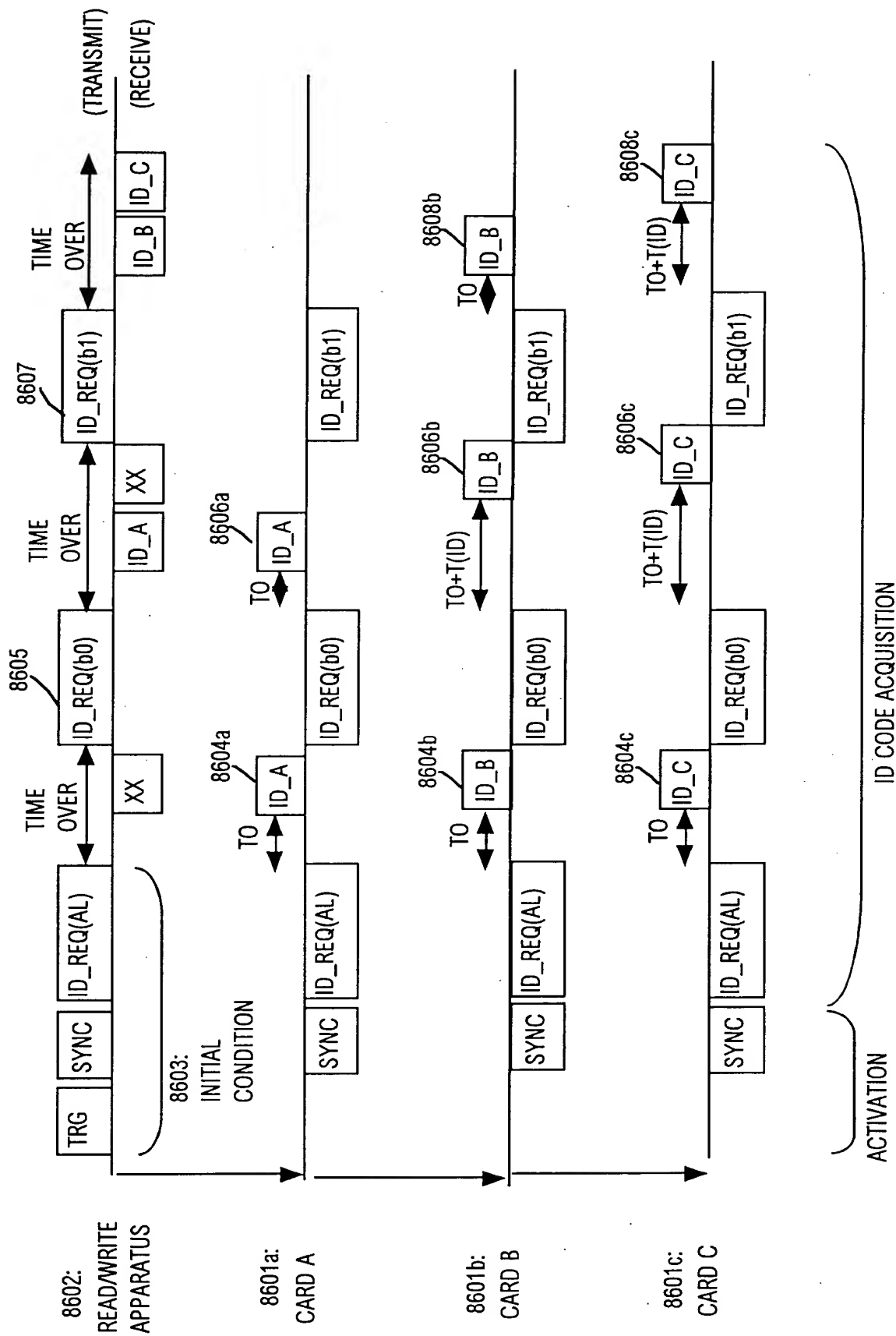


FIG 71

PRIOR ART



The schematic diagram illustrates a power supply circuit. A main power line, labeled 8901, enters from the top right. This line branches into two parallel paths. The upper path contains a resistor, labeled 8904a, followed by a diode (8903) oriented with its cathode towards the left. The lower path contains a diode (8902) oriented with its cathode towards the left, followed by a resistor (8904b). Both paths converge back to a common ground line at the bottom. A central component, represented by a square block, is connected to the junction between the two parallel paths. The entire circuit is enclosed within a rectangular frame.

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BY	CLERK
DATE	10/27/01

PRIOR ART

FIG. 73

